

## The Logic of Verification

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## I'm Michael Bolton



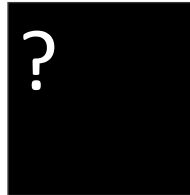
**Not the singer.**



**Not the guy  
in Office Space.**



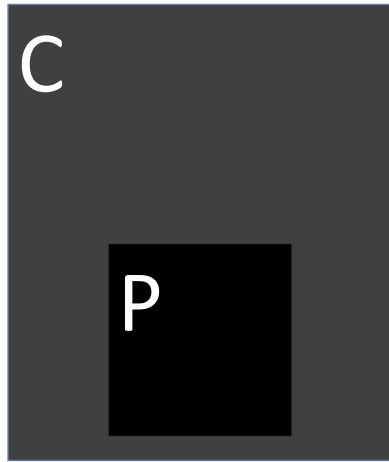
**No relation.**



We have a product of uncertain quality.

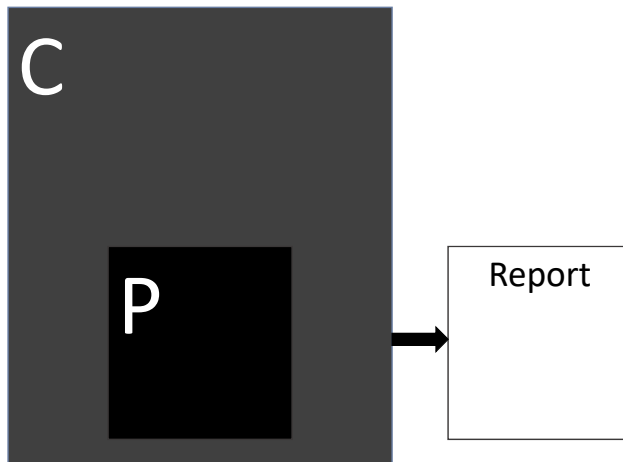


Let's call it Product P.



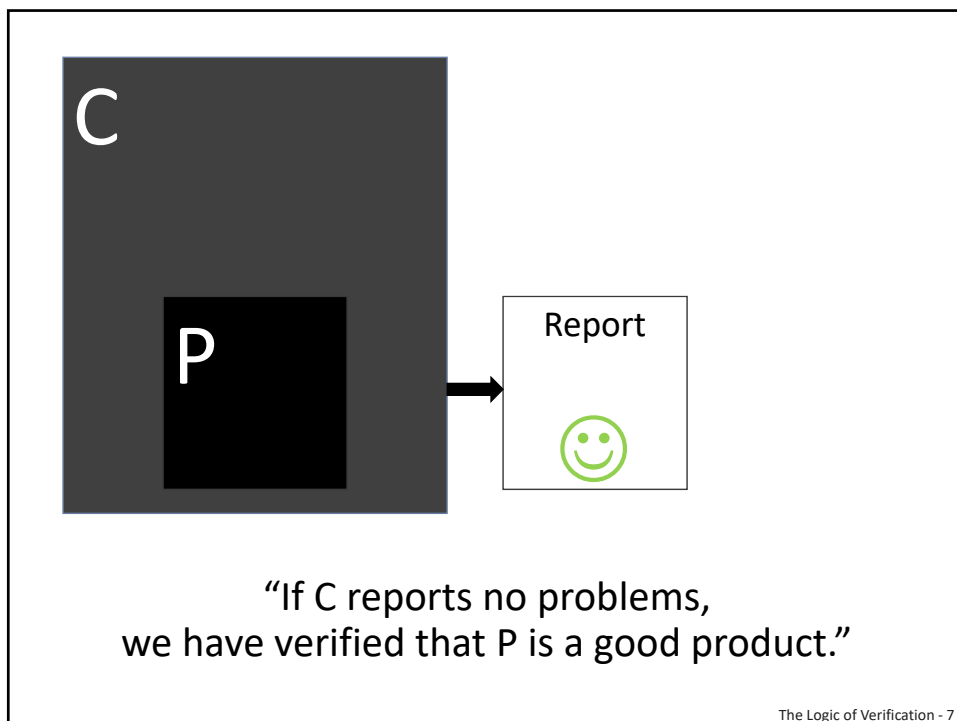
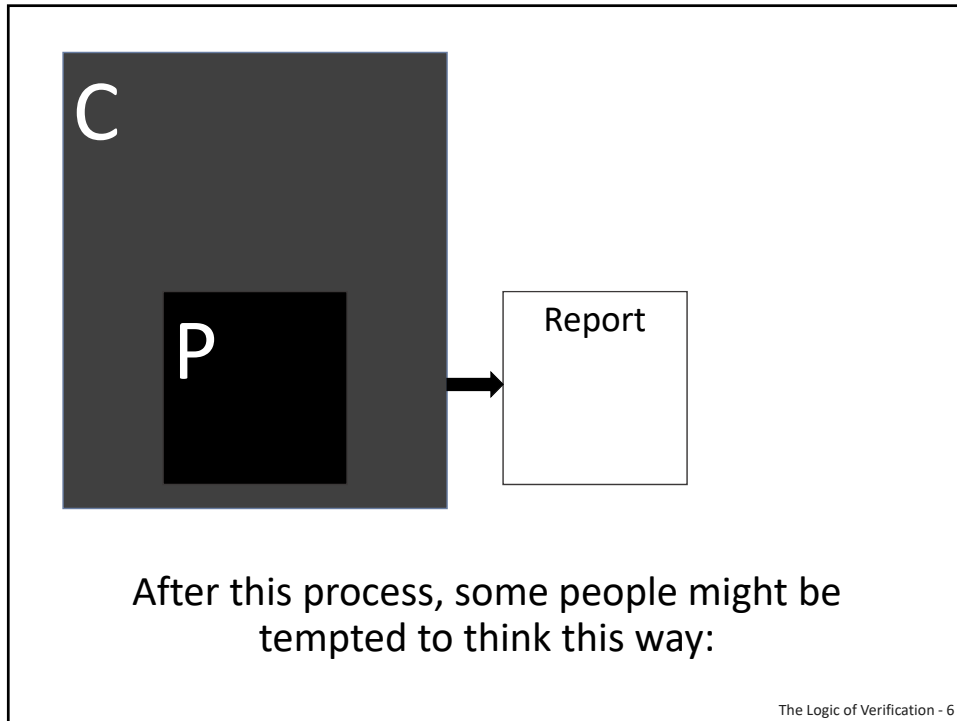
One way to evaluate P is to put it inside another system. We'll call that C (for "Checks").

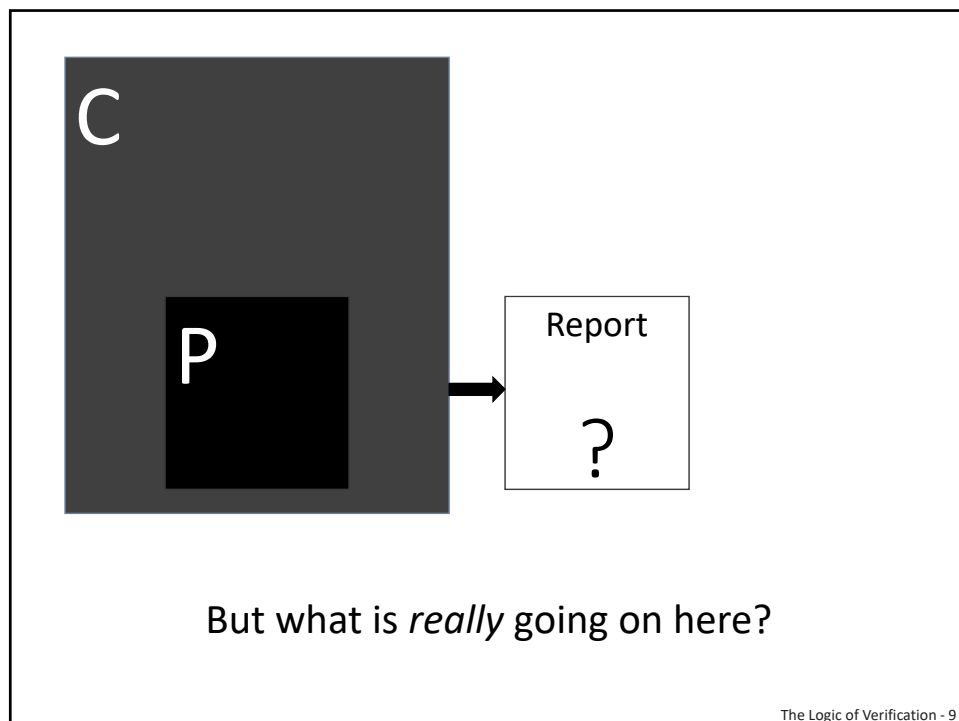
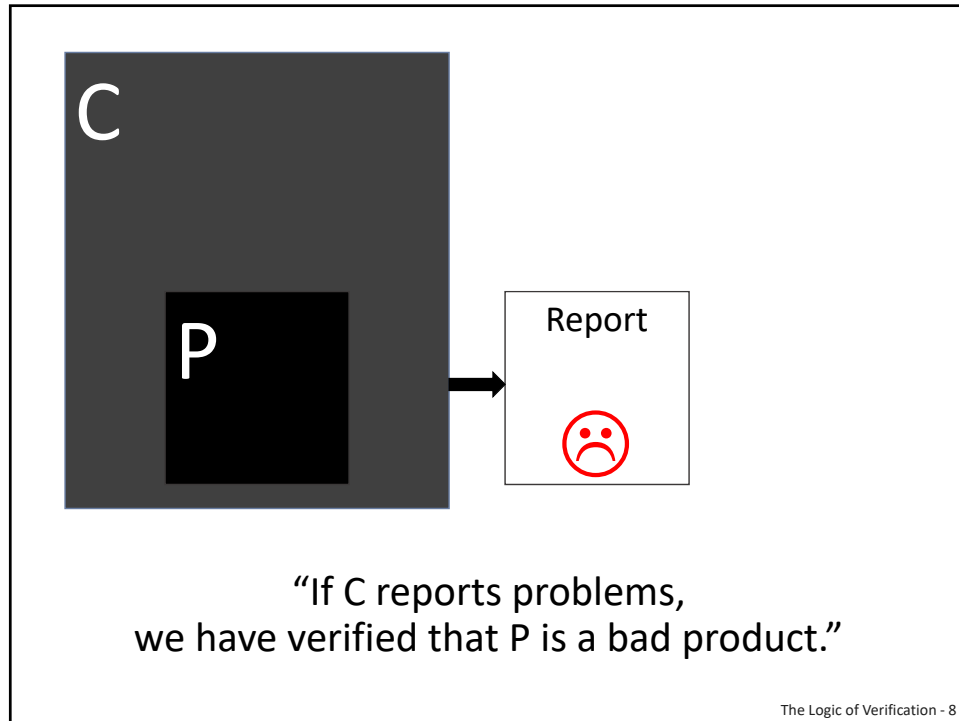
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We design C to provide input to P; to operate it; to observe it; to compare the output to a specified result, and to report on the outcome of the comparison.

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## **Verify (n.)**

- To ascertain, confirm, check, or test the truth or accuracy of
- To assert or prove to be true
- To testify to the truth of, support (a statement, law)
- To check (items of data input) for accuracy eg by having the same data keyed twice, by separate operators, and then checked by computer for discrepancies (computing)

Weird!

—*Chambers Dictionary*

## **Verification (in the RST namespace)**

### Verification (n.)

1. The process of establishing the truth of a proposition (this is universal, rather than specific to software)
2. In regulated software development, the process of comparing a product to its immediate specification

### **Verification is distinct from “validation”**

### Validation (n.)

the process of assessing a product against how well it fulfills its ultimate purposes

## What IS Verification?

- Something exists.
- Some of what exists can be known.
- Some of *that* can be described in words.
- Some of *that* can be expressed as propositions which are either true or false.



Again: verification is the process of establishing the truth or falsehood of a proposition.

## Verification isn't a feeling.

- Verification is reasoning via a logical process, within a logical system.
- $X + Y = 2$  has a **truth value** and **can be verified** as true or false **if** the values of X and Y are known, are numbers, and the conventions of arithmetic apply.
- $X + Y = 2$  may have a **truth value** that is **unverifiable** **if** the conventions of arithmetic apply, and X and Y are numbers, **but** those values are not known.
- $X + ☯ = 2$  **does not have a verifiable truth value**, if the conventions of mathematics apply.
- (We chose the ☯ symbol because it looks nice, and it starts with Y, but it stands for nothing in particular.)

## DID work is not DOES work; CAN work is not WILL work.

In a system with a non-trivial state space,  $X + Y = 2$  may be true ten times in a row, yet may be false on the next iteration. So...

- If you find  $X + Y = 2$  is true even one time, then you have verified that it **CAN** be true.
- But unless you check **EVERY POSSIBLE** state of the system, *including possible states that you don't even know are possible*, you cannot verify that  $X + Y = 2$  **WILL** be true.

## Problems with Verification

- Propositions without a truth value can't be verified.
  - "Colorless green ideas sleep furiously." (huh?)
- Statements about the future cannot be verified until we reach that future.
  - "There are no bugs in the product." (*so far*)
  - "Our checks will find all the bugs in this product." (*we hope*)
  - "Customers will be satisfied with this product." (*we believe*)



## Verifying Statements About The Future



- Go to a set point in the future
- Ask *all* customers “Were you satisfied with it?”
- Come back and report success! Hurrah!
- But even then, you can’t verify that they would *remain* satisfied *after* you asked them.

### Infinite Leap: situated fact → abstract spec

“The product is not currently in a crashed state.”

...is knowable here and now, but the fact that this is true does not mean that the product won’t crash five minutes from now, or won’t crash right now if I type the wrong key.

But what I care about is...

“The product shall not crash.”

...is timeless and applicable to many situations. It cannot be verified empirically.

## Infinite Leap: situated fact → abstract spec

“I am able to read the buttons on this screen.”

...is knowable here and now, but the fact that this is true does not mean that it will be true for all buttons, at all times, on all browsers, in every state, for every kind of person, under all lighting conditions.

But what I care about is...

“The product shall be reasonably easy to use.”

...is timeless and applicable to many situations. It cannot be verified empirically.

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## Infinite Leap: situated fact → abstract spec

“I recognize the signup screen and see nothing wrong.”

...is knowable here and now, but the fact that this is true does not mean that it will be true for every situation where that screen should be displayed, that it is compatible with every browser, and that all the links and JavaScript do the right things.

But what I care about is...

“The correct signup screen shall be displayed.”

...is timeless and applicable to many situations. It cannot be verified empirically.

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## Asymmetries: What We Can (and Can't) Verify

Verifiable	Not Verifiable
that there is a problem for some person	that there is no problem for that person
that we are not aware of a problem for some person	that there is no problem for any person
that the product did something under specific conditions that we have observed	that the product will do the same thing under conditions that we have not yet observed
that the product DID do something	that the product DOES do something
that the product CAN do something	that the product WILL do something
that we were aware of certain conditions we believed to be relevant to the test	that we were aware of all conditions relevant to the test
that a product does not meet a requirement	that a product does meet a requirement
that the product <i>appears</i> to meet a requirement to some degree	that the product definitely meets a requirement
that the product has not crashed	that the product will not crash
that we have not observed a problem in a feature so far	that there is no problem in a feature
that someone is currently satisfied with the product, based on what they know at the moment	that someone will continue to be satisfied when new knowledge is revealed
facts that might influence decisions about quality	the product's quality

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## Verification isn't exactly testing.

To say

"This product is very good"

is often like saying

"This product is very ☯ based on known variables X and Y, plus all our *assumptions* about *unknown* variables  $V_1, V_2, V_3 \dots V_{10000} \dots$  etc."

This is **unverifiable**, but it may be **testable**.

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Testing is  
**way more**  
than verification

## Oracle-Related Heuristics

- An **oracle** is a heuristic for recognizing a bug when you encounter it.
- A **trigger heuristic** is a means of becoming aware that a situation requires your attention.
- A **radiator heuristic** is a means of conveying or representing information that you need to solve a problem.
- A **decider heuristic** is a means of deciding what to do to solve a problem.
- Thus there are **trigger oracles** and **radiator oracles** and **decider oracles**.

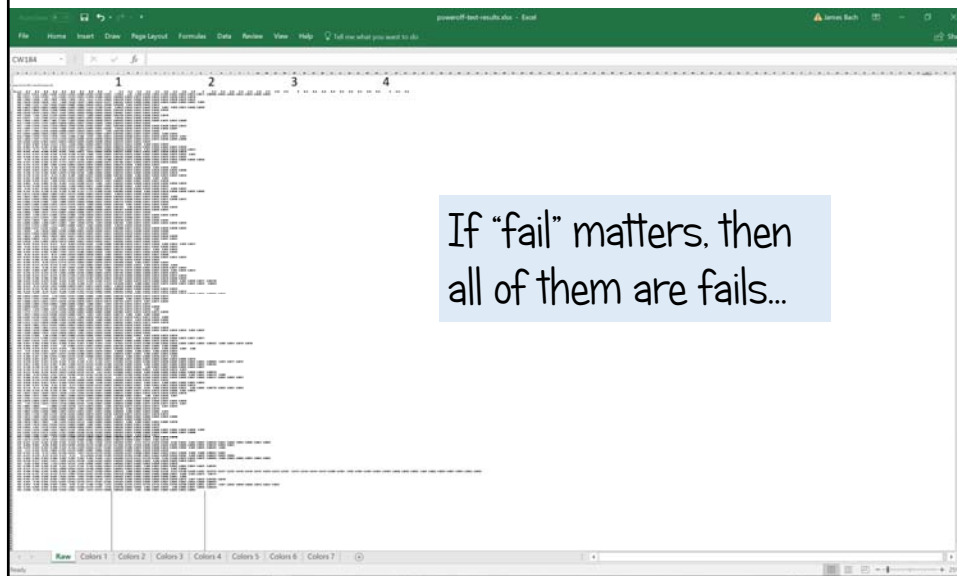
## Verifications Can Be Good Triggers But Are Poor Deciders or Radiators

- A failing check definitely tells you that you have work to do. You must investigate. That's a **trigger**.
- Failing checks almost never **decide** that the software IS bad, because our first question is "Why did it fail? Could it be broken?" *The humans ultimately decide.*
- Log files, screens, and data displays are **radiators**. They are not subject to "pass/fail" but rather must be absorbed, interpreted, pondered, in loops.
- **Triggers** combined with **radiators** are an especially powerful combination.

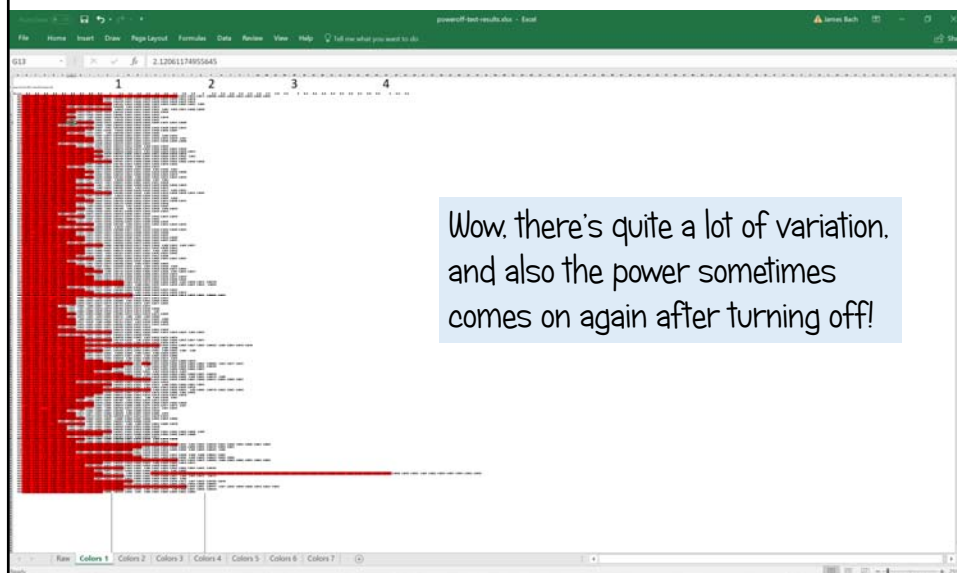
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2	Session	0.1	0.2	0.3	0.4	0.5	0.6	0.7	0.8	0.9	1	1.1	1.2	1.3	1.4	1.5	1.6	1.7	1.8
3	1801	6.92757	6.92747	6.93079	6.92967	6.92944	6.93281	6.93098	6.92878	6.92903	6.92624	6.93171	6.937	6.9538	6.99196	7.00324	3.13202	0.40431	0.00556
4	1802	6.91471	6.9135	6.91359	6.915	6.91361	6.91414	3.85074	0.78258	0.01005	0.00495	0.00479	0.00466	0.00453	0.00444	0.00433	0.00425	0.00419	0.00411
5	1803	7.18369	7.18196	7.1825	7.18237	7.18104	7.18102	7.18112	3.6575	0.00527	0.00504	0.00479	0.00458	0.00444	0.00428	0.00419	0.00411	0.00403	0.00395
6	1804	7.05736	7.05589	7.05536	7.0527	7.0538	7.05186	7.05378	0.00416	0.00388	0.00381	0.00378	0.00378	0.00373	0.00367	0.00362	0.00357	0.00352	0.00347
7	1805	7.14868	7.11577	7.11352	3.01661	0.63501	0.00803	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404	0.00404
8	1806	6.88571	6.88733	6.88973	6.88838	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801	6.88801
9	1808	6.98735	7.00024	7.06132	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238	6.5238
10	1809	6.87431	6.87234	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001	5.22001
11	1810	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504	7.23504
12	1811	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574	6.92574
13	1812	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787	7.18787
14	1813	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983	7.11983
15	1814	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458	7.2458
16	1815	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091	7.16091
17	1816	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777	7.19777
18	1817	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045	6.84045
19	1818	7.19809	7.19769	7.19792	7.19748	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757	7.19757
20	1819	7.10956	7.11011	7.11256	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173	7.11173
21	1820	6.84502	5.01045	0.70642	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554	0.01554
22	1821	15.0995	15.0997	11.0239	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471	1.5471
23	1822	15.0865	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091	15.1091
24	1823	15.1312	15.111	15.051	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018	15.018
25	1824	14.6495	14.6495	14.6484	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644	14.644
26	1825	14.2415	14.2474	14.2438	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248	14.248
27	1826	15.4638	15.4631	15.4634	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462	15.462
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29	1828	15.1675	15.2033	15.2682	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283	15.283
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32	1831	14.8435	14.8576	14.8973	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945	14.945
33	1832	14.1206	14.1159	14.1092	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884	13.884
34	1833	14.1796	14.1765	14.1871	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17	14.17
35	1834	15.2331	15.1896	15.1462	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505	10.505
36	1835	15.3283	15.3295	15.3261	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028	7.028
37	1836	14.8854	14.845	14.8032	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798	14.798
38	1838	15.2423	15.2438	15.2423	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235	15.235
39	1839	14.284	14.2817	14.2828	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283	14.283
40	1840	14.1447	14.1354	14.1438	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149	14.149
41	1841	6.87141	6.81458	6.80402	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829	4.58829
42	1842	7.03472	7.03375	7.03335	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401	7.03401
43	1843	6.99547	6.99499	6.99362	6.99335	6.99356	6.99323	3.36603	0.25791	0.00946	0.00542	0.0061234	0.00588	0.00564	0.00544	0.00525	0.00511	0.00488	0.00475
44	1844	6.44865	6.44238	6.44082	5.6285	1.48802	0.03732	0.00463	0.00608	0.00567	0.00539	0.0051713	0.00501	0.00483	0.00471	0.00459	0.00449	0.00441	0.00433
45	1845	6.49973	6.49901	6.49699	6.49722	5.96144	0.24735	0.00739	0.0058	0.00505	0.00494	0.004694	0.0047	0.00462	0.00454	0.00445	0.00437	0.00429	0.00421
46	1846	6.57567	6.57232	6.57289	6.57175	6.5714	4.39143	0.0273	0.00821	0.00605	0.0055	0.0051323	0.005	0.00484	0.00477	0.00468	0.0046	0.00453	0.00445
47	1847	6.93647	6.96609	7.01036	7.01131	6.36602	0.0183	0.00693	0.00588	0.00586	0.00526	0.0051071	0.00489	0.00473	0.00459	0.00449	0.00441	0.00433	0.00425
48	1848	6.99547	6.99499	6.99362	6.99335	6.99356	6.99323	3.36603	0.25791	0.00946	0.00542	0.0061234	0.00588	0.00564	0.00544	0.00525	0.00511	0.00488	0.00475
49	1849	6.44865	6.44238	6.44082	5.6285	1.48802	0.03732	0.00463	0.00608	0.00567	0.00539	0.0051713	0.00501	0.00483	0.00471	0.00459	0.00449	0.00441	0.00433
50	1850	6.49973	6.49901	6.49699	6.49722	5.96144	0.24735	0.00739	0.0058	0.00505	0.00494	0.004694	0.0047	0.00462	0.00454	0.00445	0.00437	0.00429	0.00421
51	1851	6.57567	6.57232	6.57289	6.57175	6.5714	4.39143	0.0273	0.00821	0.00605	0.0055	0.0051323	0.005	0.00484	0.00477	0.00468	0.0046	0.00453	0.00445
52	1852	6.93647	6.96609	7.01036	7.01131	6.36602	0.0183	0.00693	0.00588	0.00586	0.00526	0.0051071	0.00489	0.00473	0.00459	0.00449	0.00441	0.00433	0.00425
53	1853	6.99547	6.99499	6.99362	6.99335	6.99356	6.99323	3.36603	0.25791	0.00946	0.00542	0.0061234	0.00588	0.00564	0.00544</				

## A “zoom blink” radiator oracle

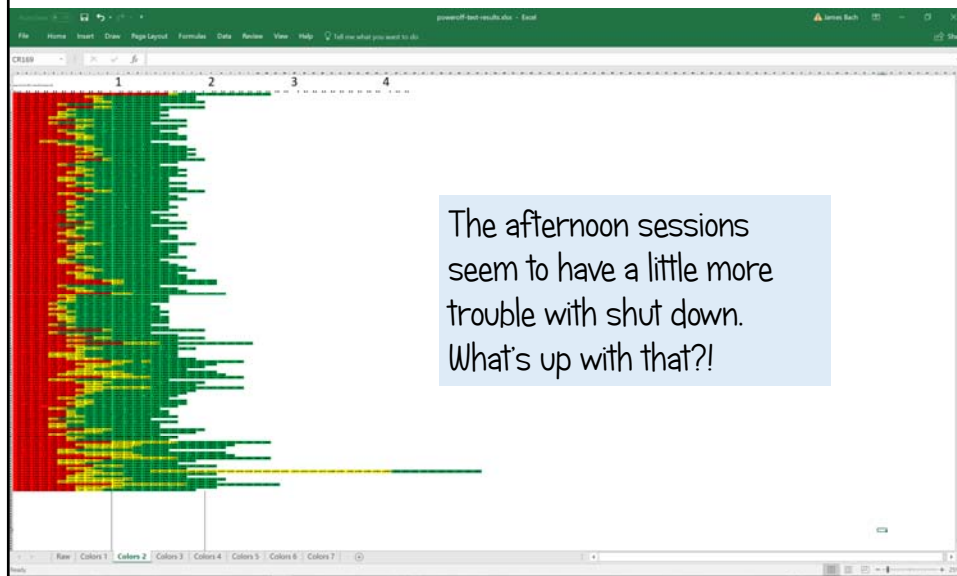


## Colored for $>.01$ vs. $\leq .01$

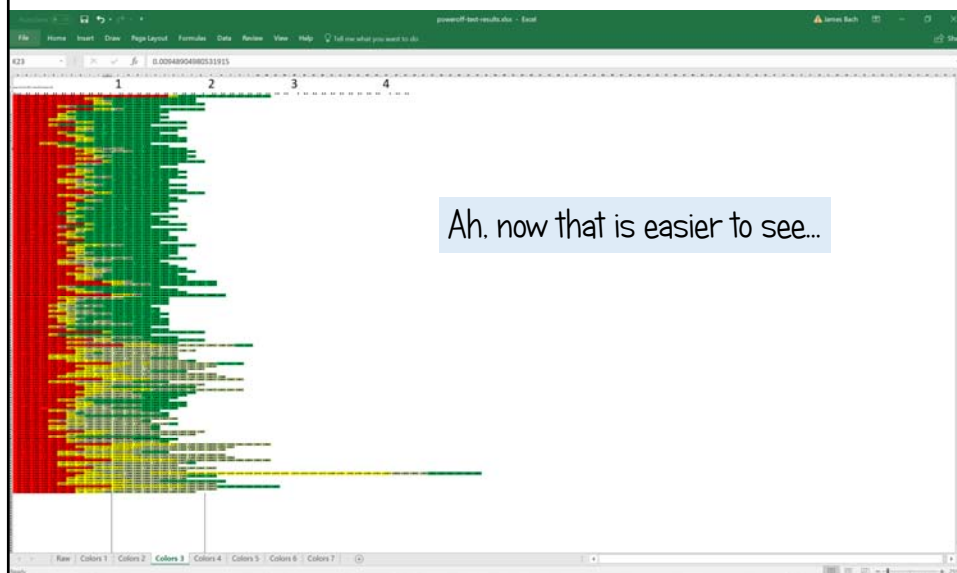


The Logic of Verification  
Michael Bolton and James Bach  
Heisenbug Conference, St. Petersburg, May 2018

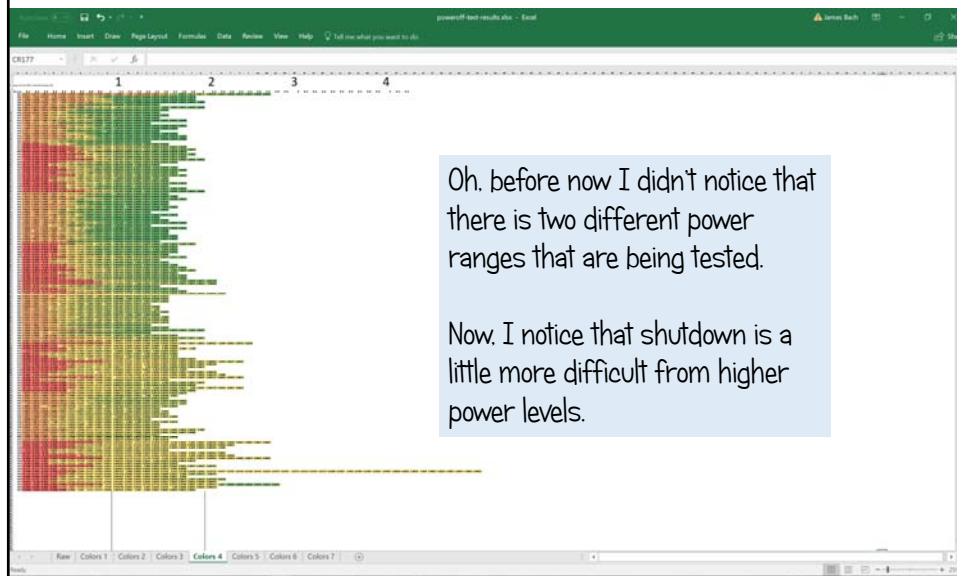
**$\geq 1$  vs.  $< 1$  &  $> .01$  vs.  $\leq 0.01$**



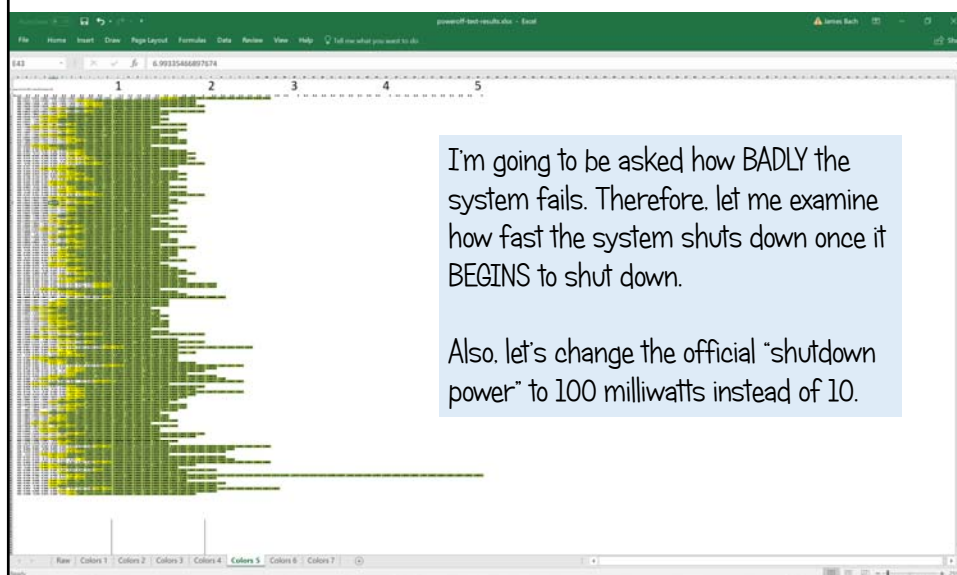
**$\geq 1$  vs.  $< 1$  &  $> .01$  vs.  $\leq .01$  &  $> .08$  vs.  $\leq 0.08$**   
**"low bridge heuristic"**



## Defocusing: used default Excel coloring

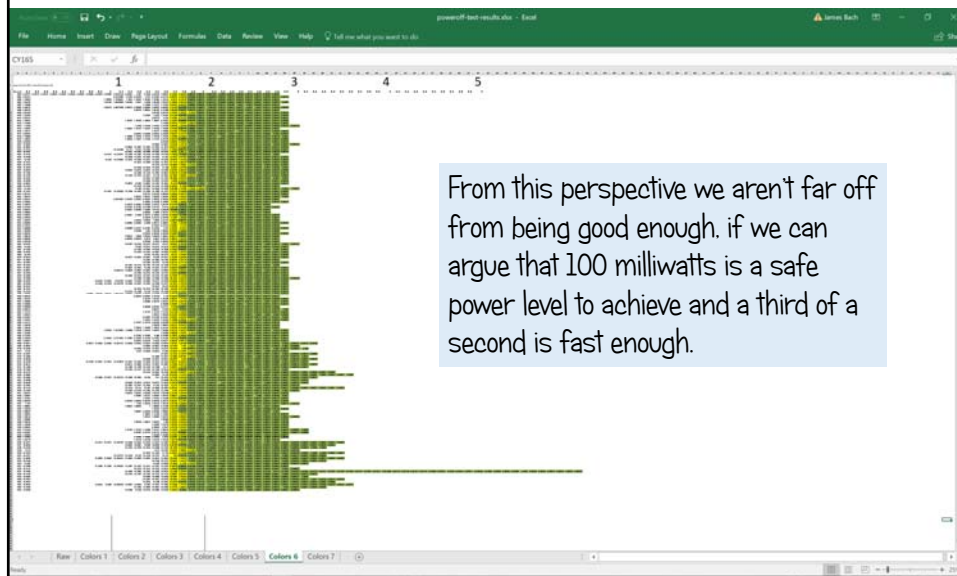


**=< 90% of start vs. >= 0.1**

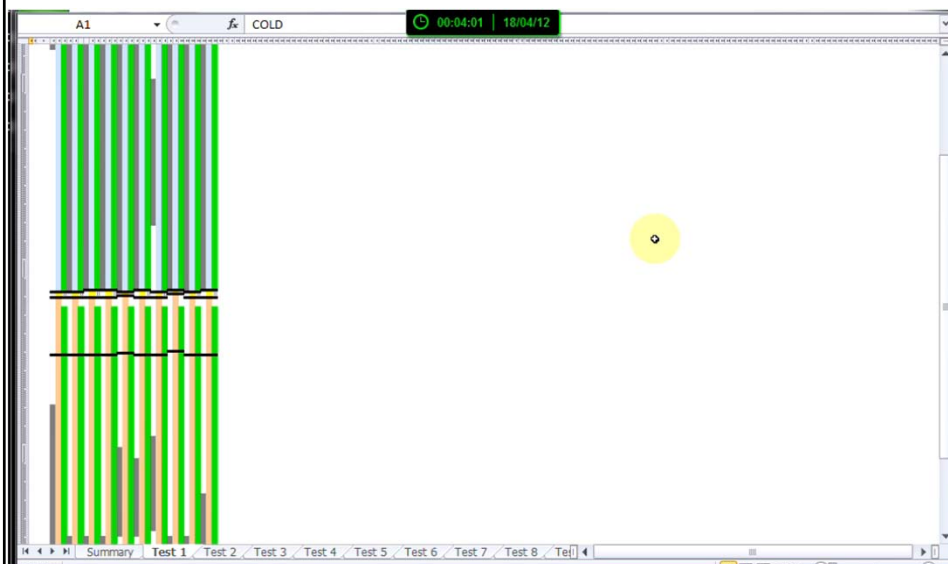




## Centered on first $\leq 90\%$ measurement



## Triggers + Radiators Facilitate Testing



## Triggers + Radiators Facilitate Testing

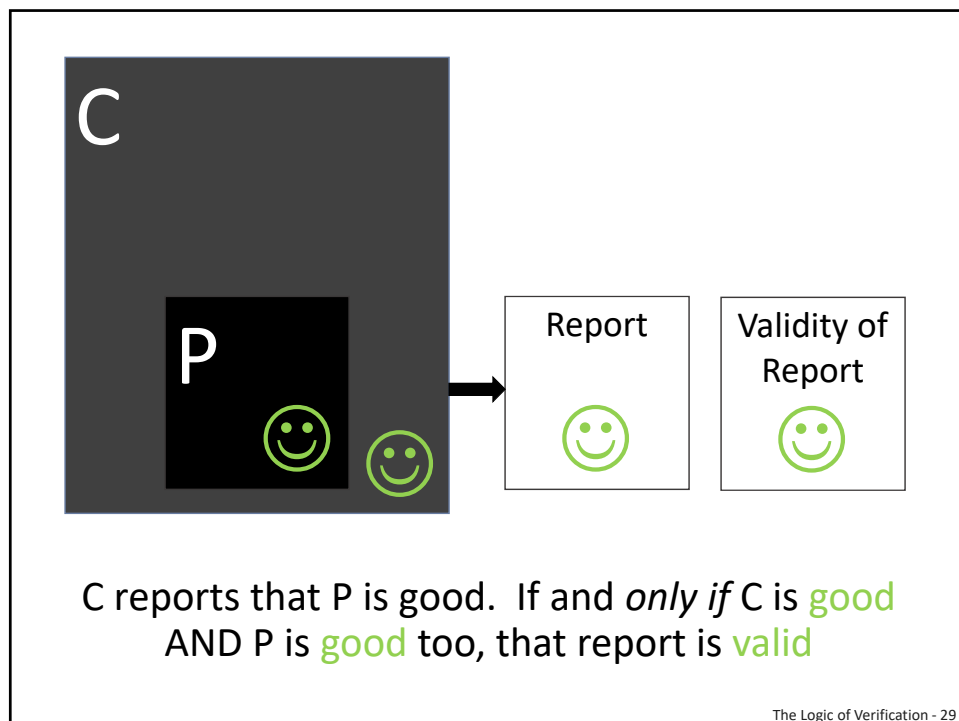
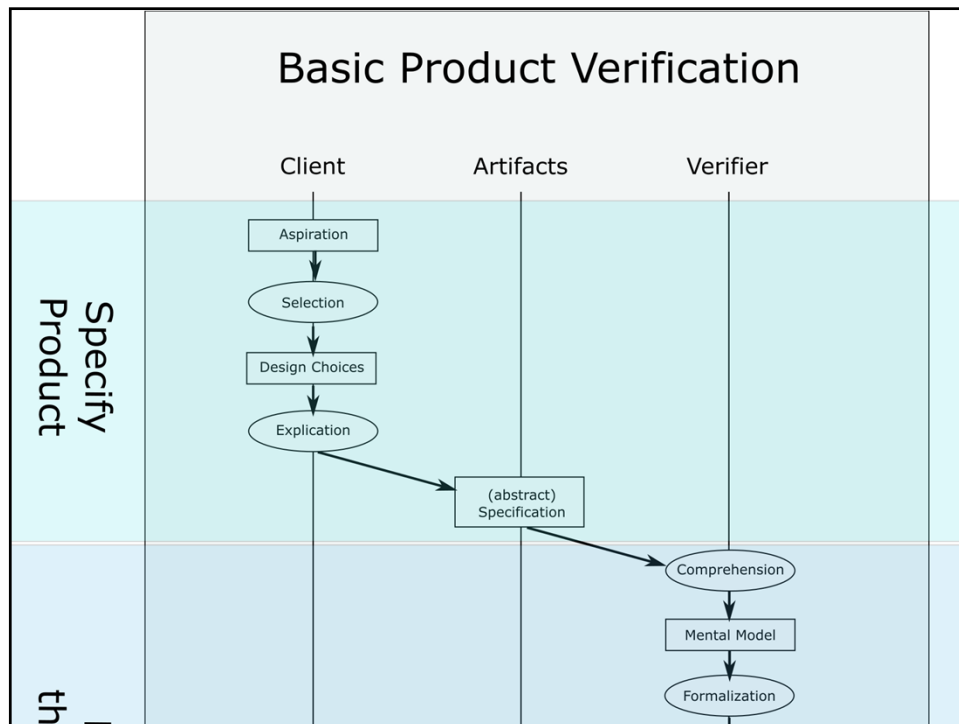
	A	B	C	D	E	F	G	H	I	
89	STD. DEV.	n/a	0.41	1.83	0.29	0.47	0.00	0.00	18.08	
90										
91	Test	Catheter	Marker Number	Catheter Type	Number of Electrodes					
92	6	6	6	Crescent	7					
93										
94		Time Until Within 5 Deg	Acc. After 3 Secs.	Time Until Restabilized	Stable Accuracy (HOT)	Stable Accuracy (COOL)	Stable Variance (HOT)	Stable Variance (COOL)	Sample Size (HOT)	Sam
95	1	00.6	00.78	08.7	-00.39	13.97	00.00	00.00	88	
96	2	00.8	00.66	07.5	-00.42	13.97	00.00	00.01	103	
97	3	00.5	00.88	09.9	-00.09	13.98	00.00	00.00	77	
98	4	00.6	00.00	07.4	-00.91	13.10	00.00	00.00	101	
99	5	00.7	-00.07	06.7	-00.69	13.90	00.00	00.00	110	
100	6	00.6	-00.27	05.3	-00.71	13.91	00.00	00.00	123	
101	7	00.6	-00.29	06.1	-00.81	14.30	00.00	00.00	114	
102	8	n/a	n/a	n/a	n/a	n/a	n/a	n/a	n/a	
103	9	n/a	n/a	n/a	n/a	n/a	n/a	n/a	n/a	
104	10	n/a	n/a	n/a	n/a	n/a	n/a	n/a	n/a	
105	MEAN	0.63	0.24	7.37	-0.57	13.88	0.00	0.00	102.29	
106	STD. DEV.	0.09	0.47	1.44	0.27	0.34	0.00	0.00	14.50	
107										
108	Test	Catheter	Marker Number	Catheter Type	Number of Electrodes					
109	7	7	7	Circular	10					
110										
111		Time Until Within 5 Deg	Acc. After 3 Secs.	Time Until Restabilized	Stable Accuracy (HOT)	Stable Accuracy (COOL)	Stable Variance (HOT)	Stable Variance (COOL)	Sample Size (HOT)	Sam
112	1	n/a	19.06	12.1	16.90	14.65	00.00	00.00	52	
113	2	n/a	18.48	11.0	16.50	14.66	00.00	00.00	61	
114	3	n/a	18.86	11.8	16.90	15.17	00.00	00.00	53	
115	4	n/a	17.54	08.9	15.82	13.90	00.01	00.02	82	
116	5	n/a	18.28	11.2	16.40	14.65	00.00	00.00	60	
117	6	n/a	18.30	09.7	16.10	14.45	00.00	00.00	74	
118	7	n/a	18.50	11.8	16.30	14.27	00.00	00.00	54	
119	8	n/a	19.16	12.7	16.36	14.43	00.00	00.01	45	
120	9	n/a	18.98	14.4	16.84	15.97	00.35	00.00	33	

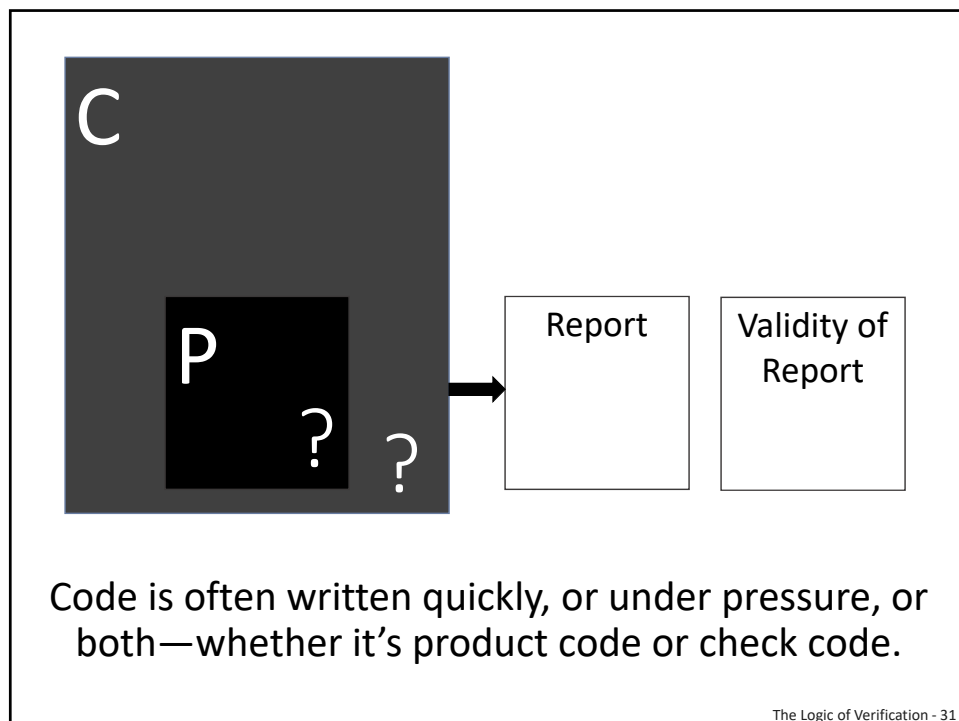
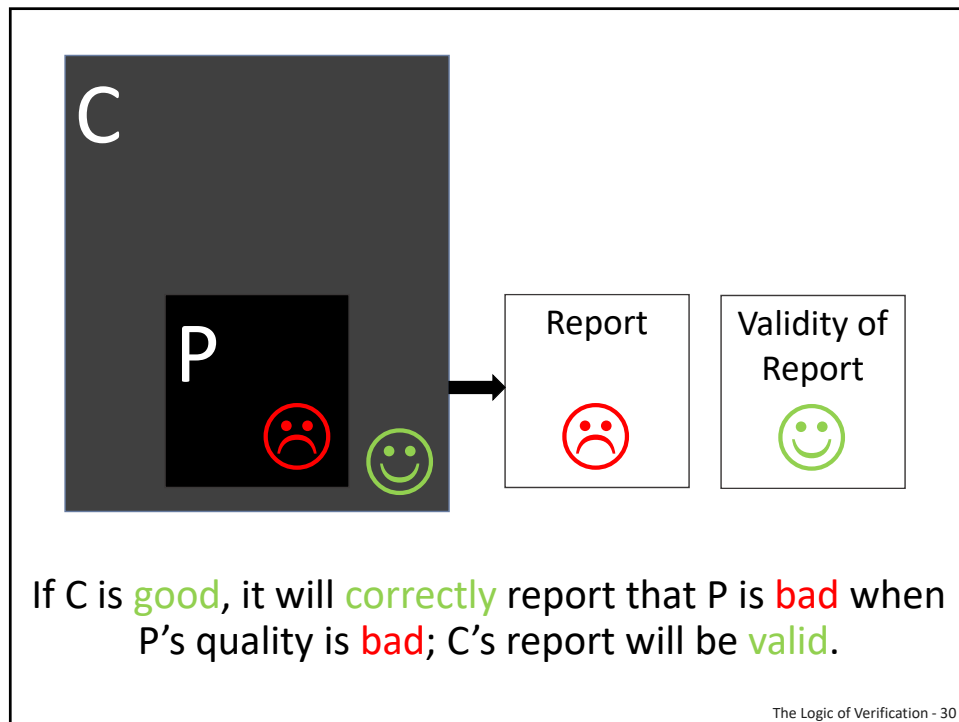
The Logic of Verification - 24

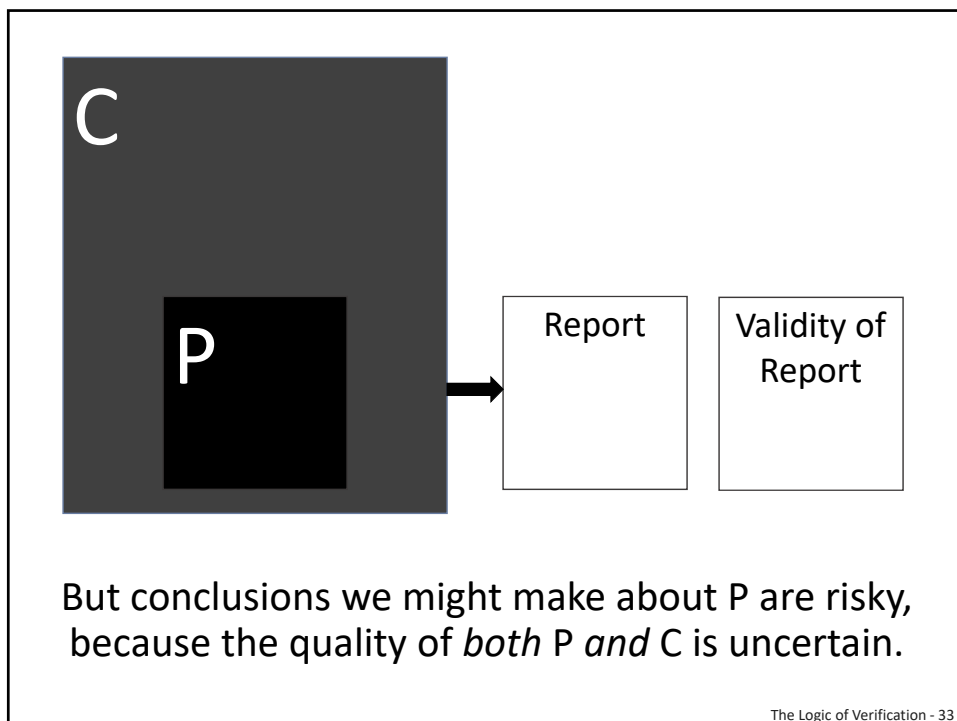
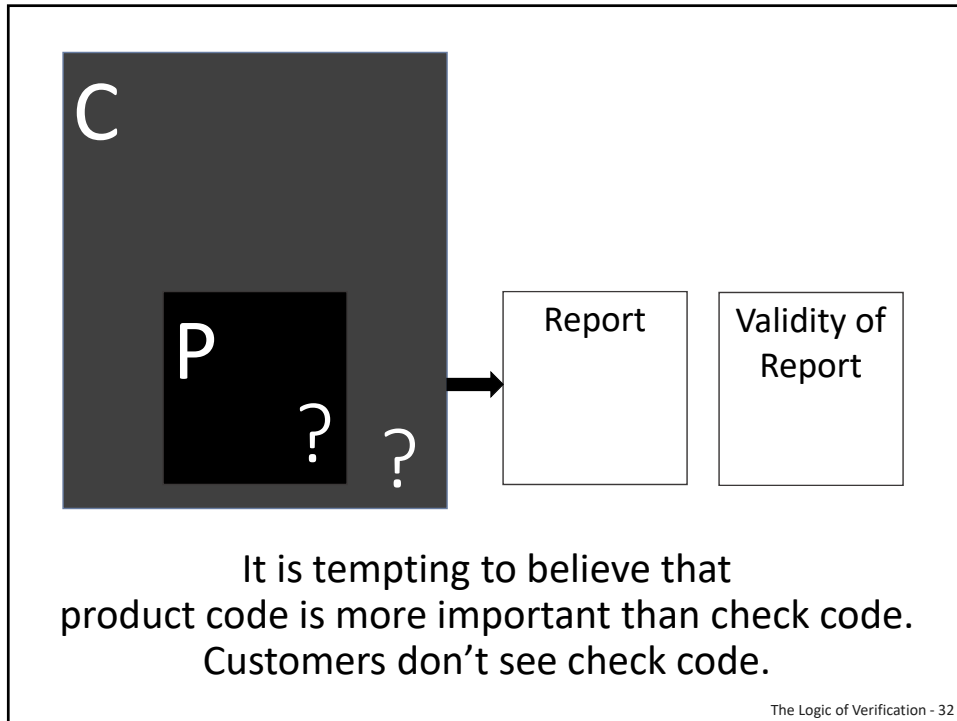
Entity	Definition
<b>Client</b>	Person who matters; whom we serve.
<b>Aspirations</b>	Ideas and values within our clients which relate to want they want.
<b>Design Choices</b>	Choices made by the client, or on the client's behalf, about what to ask for in the product, based on (possibly contradictory) aspirations.
<b>Specification</b>	Abstract statements that represent design choices
<b>Assertion/Example</b>	Situated proposition or artifact that is consistent with some specification.
<b>Product</b>	An artifact intended to fulfill the specification to a reasonable and acceptable degree.
<b>Check (verification)</b>	An algorithmic process that corroborates or refutes an assertion about a particular product in a particular situation.

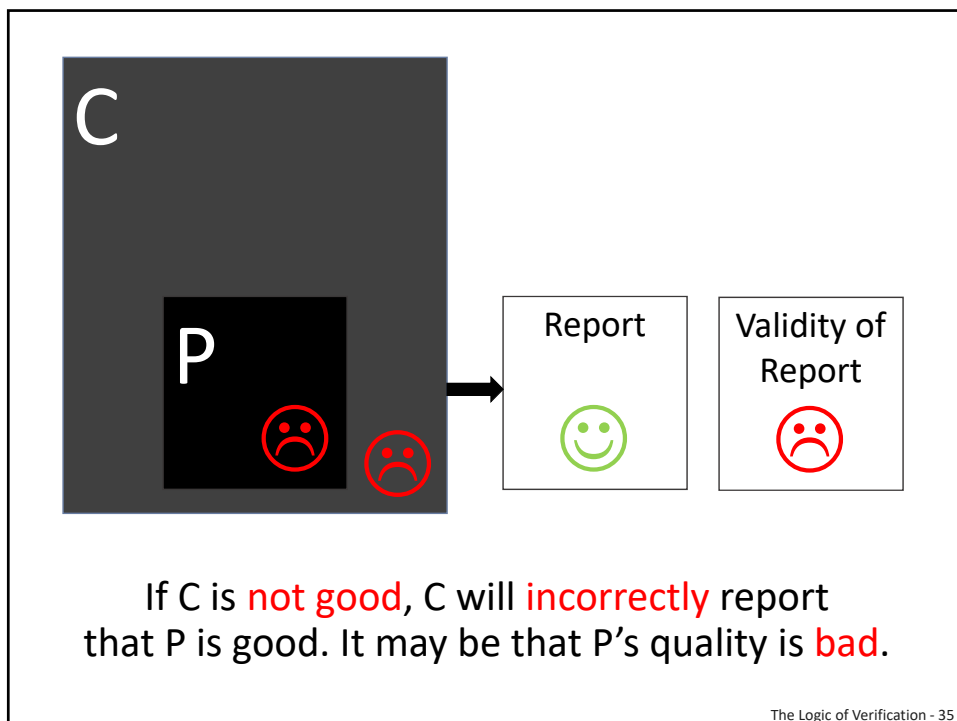
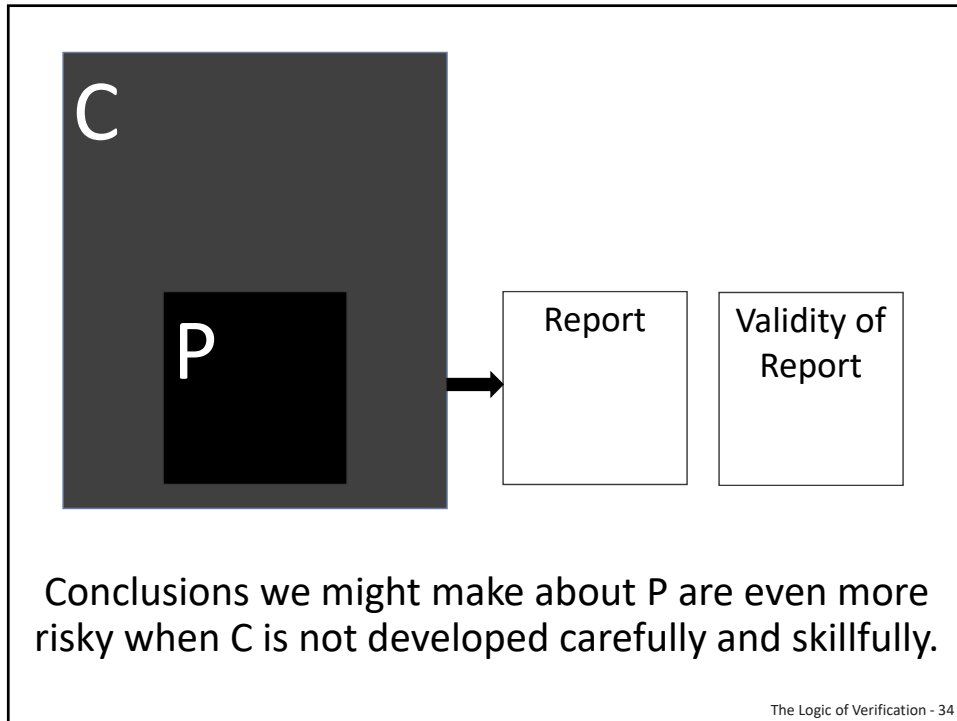
The Logic of Verification - 27

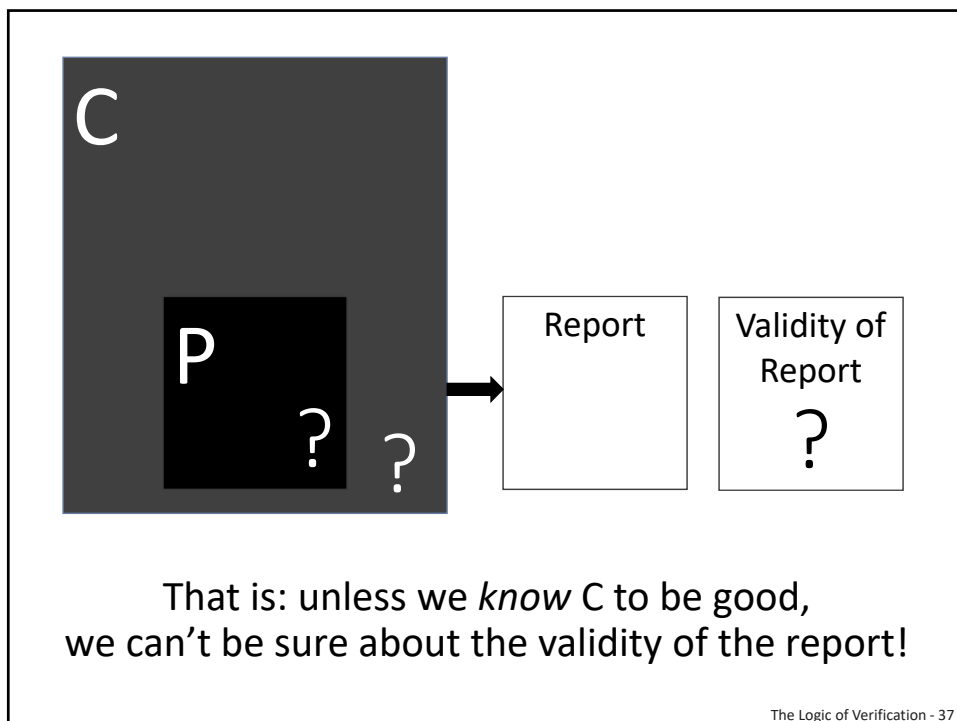
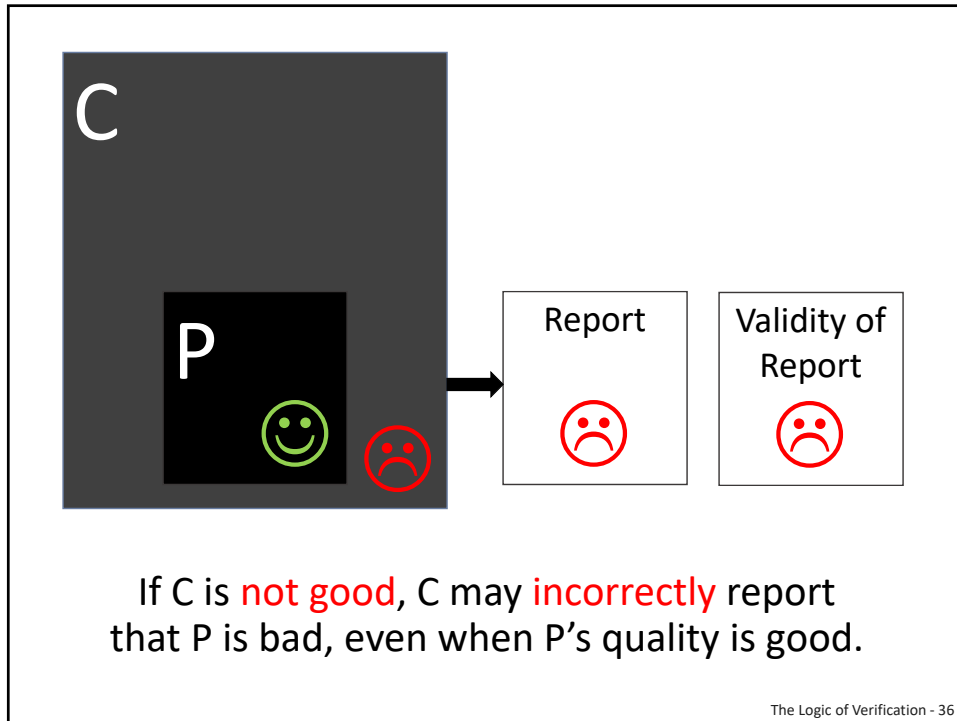
The Logic of Verification  
Michael Bolton and James Bach  
Heisenbug Conference, St. Petersburg, May 2018

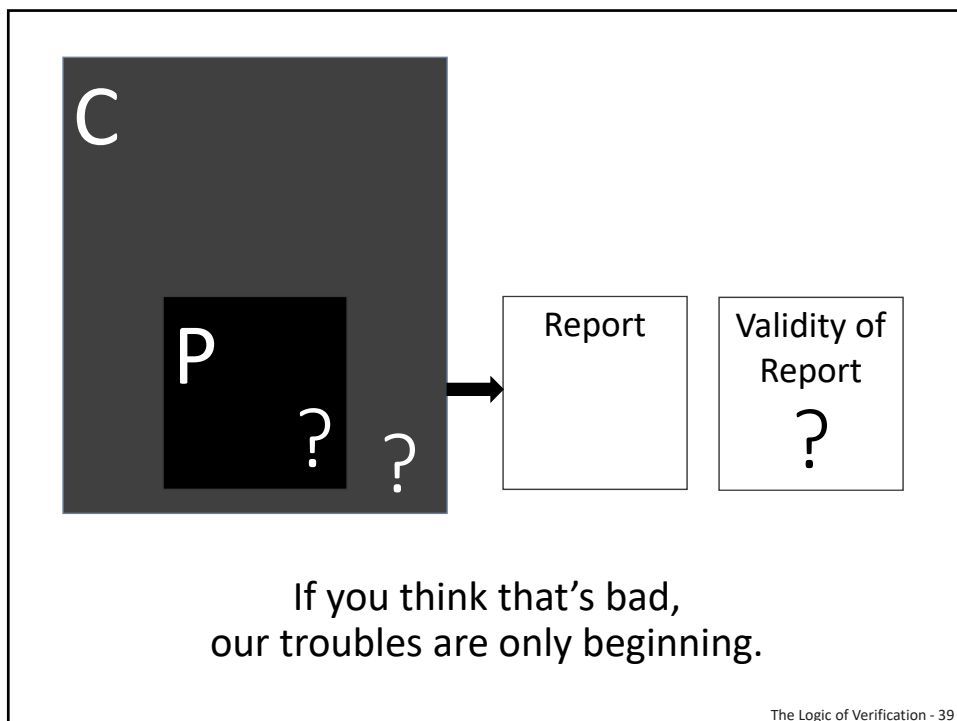
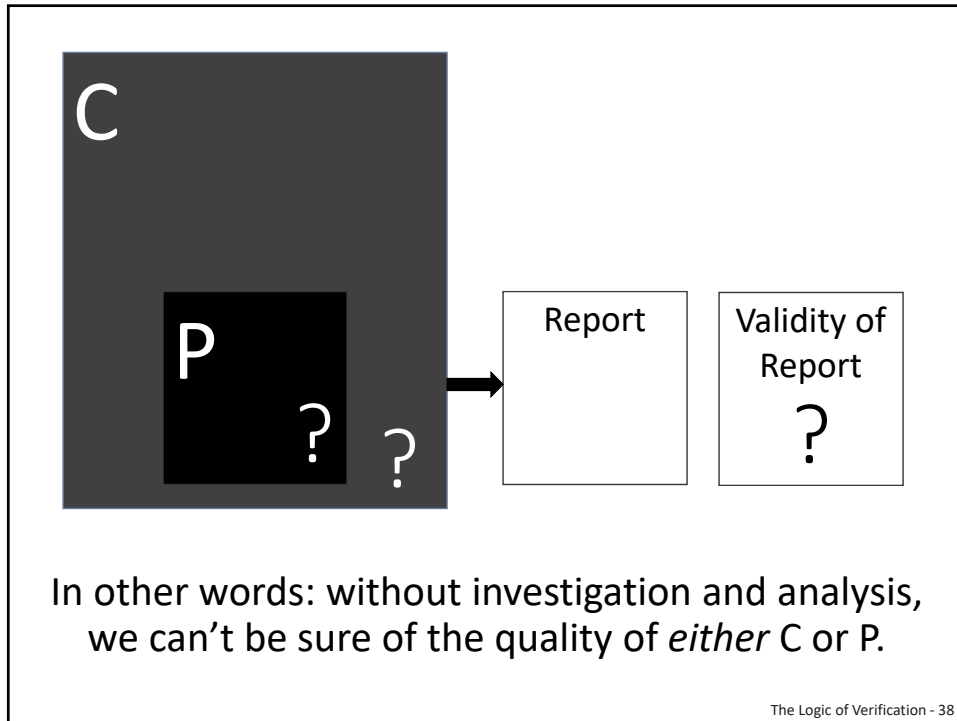




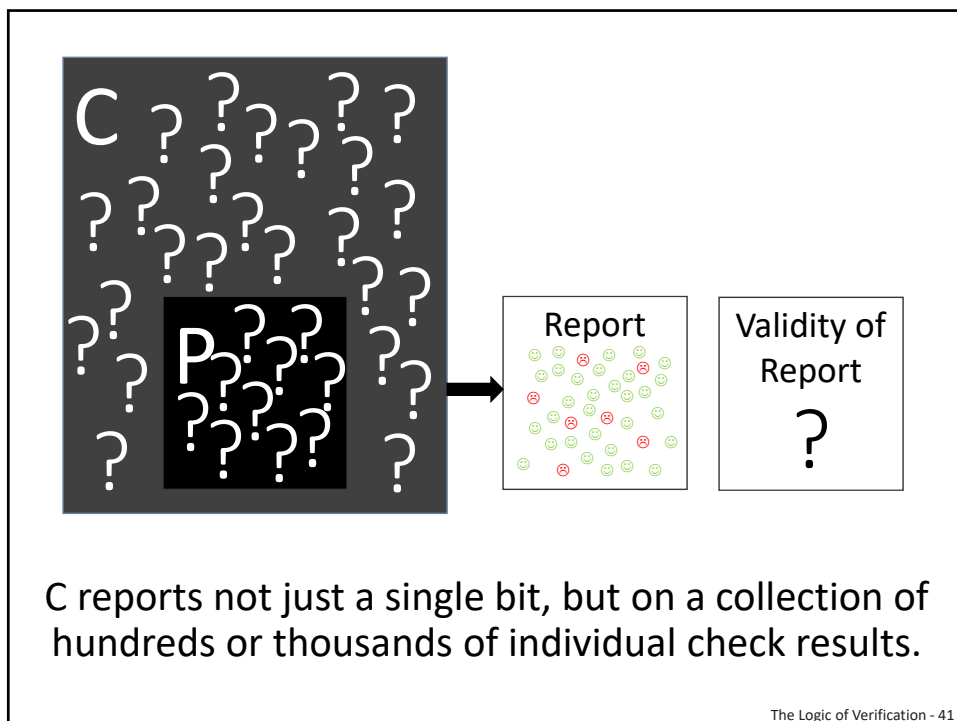
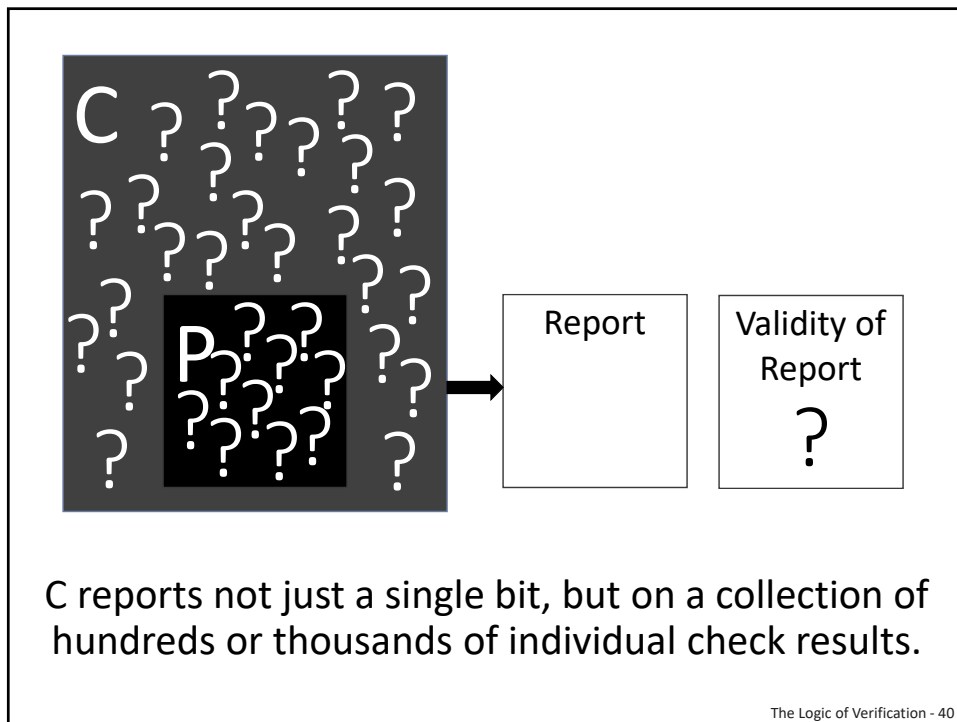












The diagram shows a large dark rectangle labeled 'C' (Checks) containing many white question marks. Inside this rectangle is a smaller black rectangle labeled 'P' (Product) containing many white question marks. An arrow points from the 'C' rectangle to a box labeled 'Report' which contains a collection of green and red smiley faces. To the right of the 'Report' box is another box labeled 'Validity of Report' containing a large question mark.

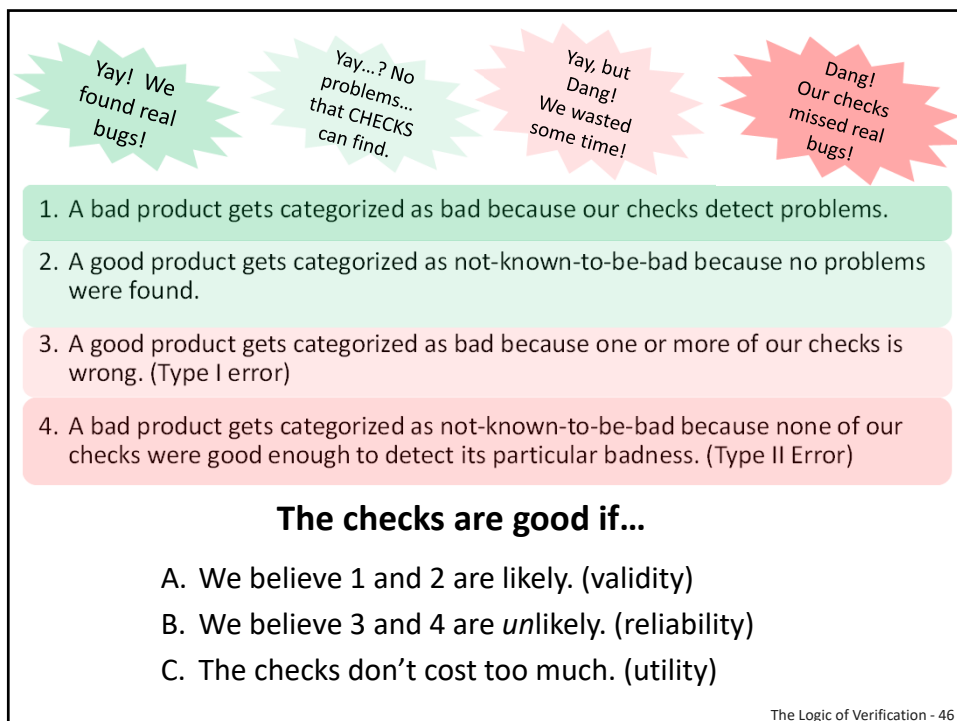
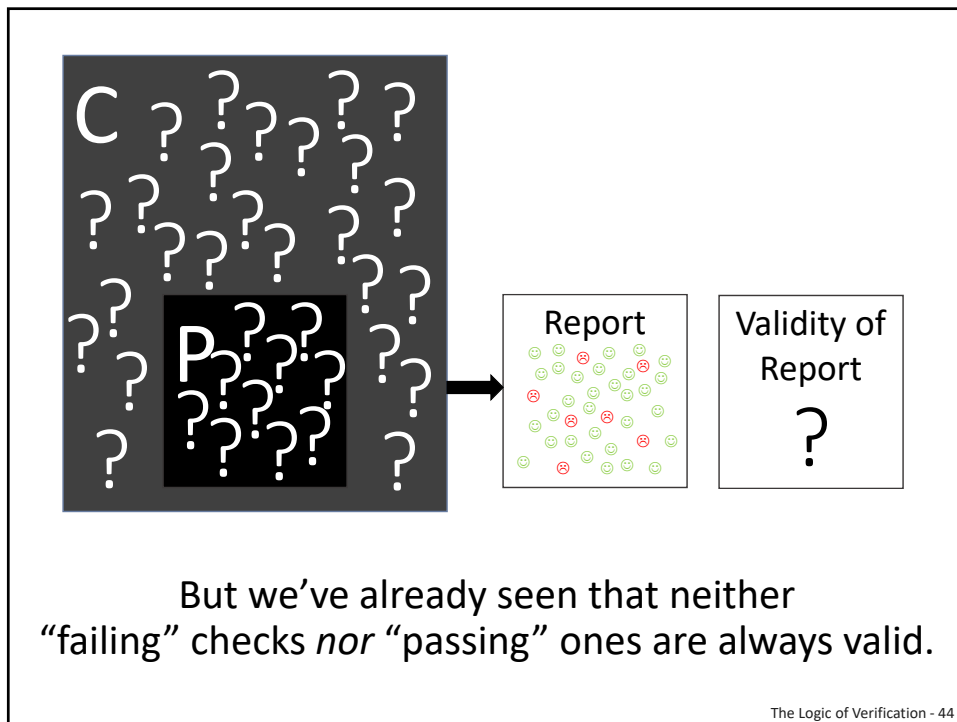
When a set of checks reports a failure, a responsible tester will not immediately report a bug.

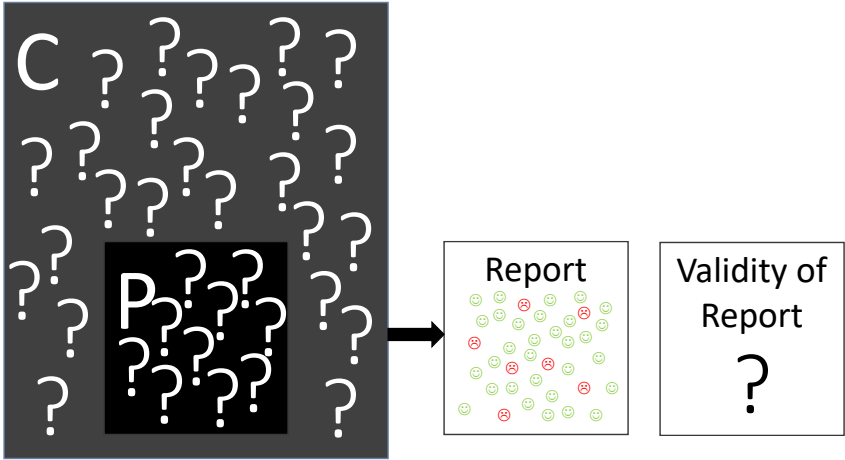
The Logic of Verification - 42

The diagram is identical to the one on slide 42, showing a large dark rectangle labeled 'C' (Checks) containing many white question marks, with a smaller black rectangle labeled 'P' (Product) inside it. An arrow points from the 'C' rectangle to a box labeled 'Report' which contains a collection of green and red smiley faces. To the right of the 'Report' box is another box labeled 'Validity of Report' containing a large question mark.

Instead, the tester must investigate and ask if this is a real problem in the product, or a problem with C.

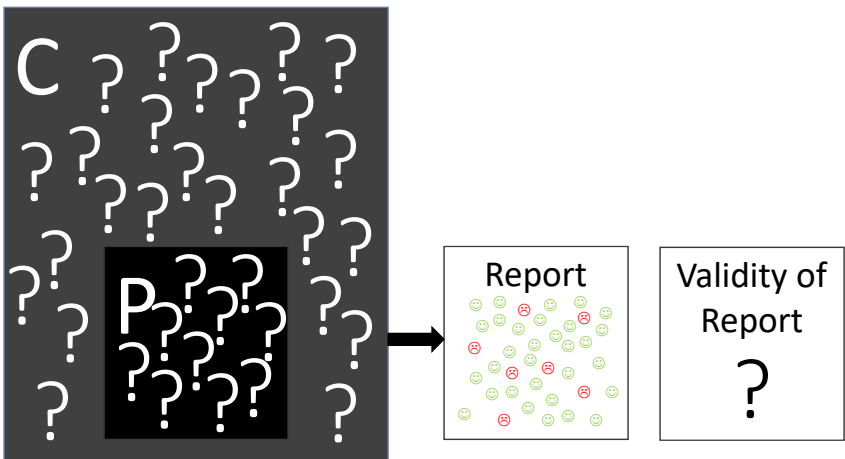
The Logic of Verification - 43





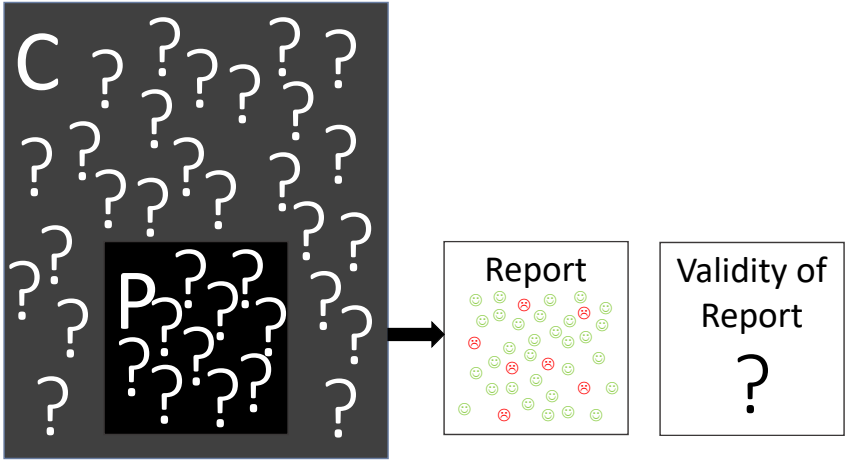
Some people dismiss checks that intermittently report failures as “flaky checks”.

The Logic of Verification - 47



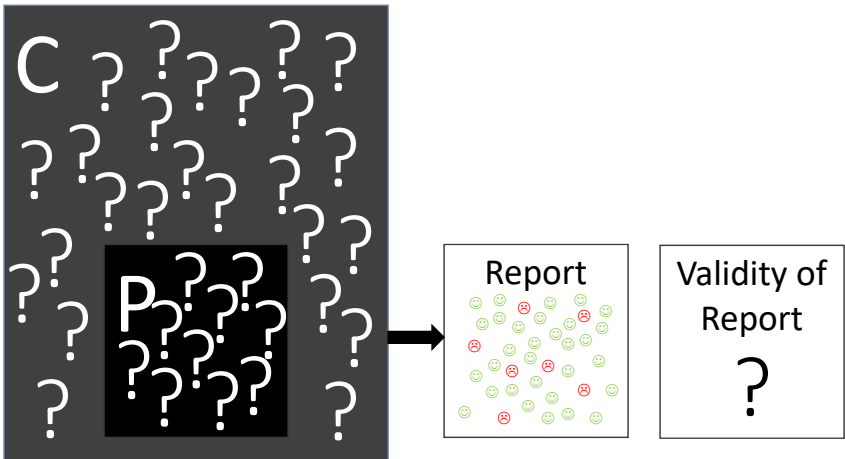
What are we doing to test the idea that “flaky checks” are non-problems?

The Logic of Verification - 48



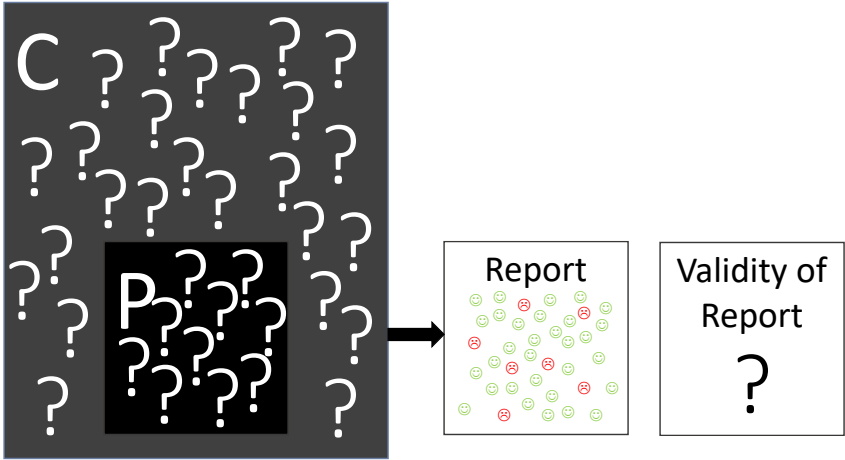
And if checks are consistently “flaky”,  
why run them at all?

The Logic of Verification - 49



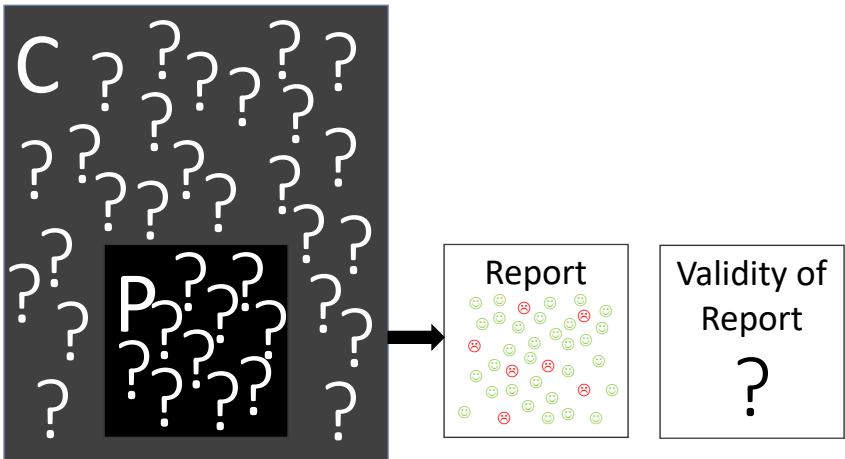
What are we doing to test the idea  
that reports of “passing” checks are valid?

The Logic of Verification - 50



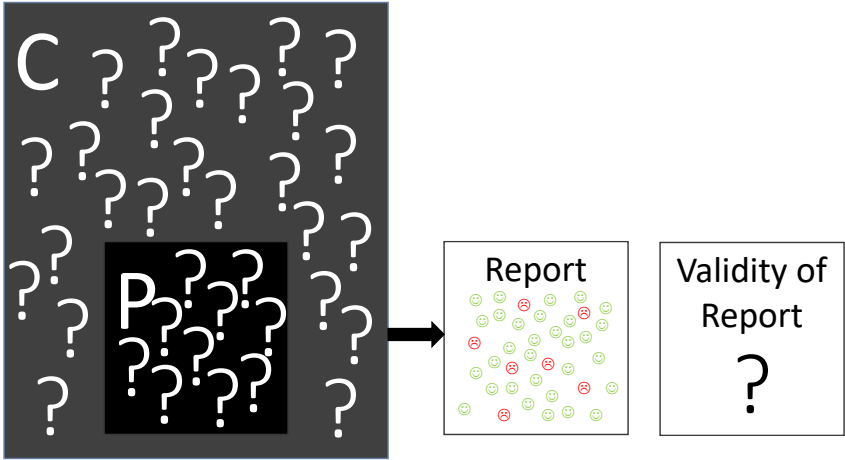
What are we doing to use checks more powerfully—  
to check more broadly and deeply?

The Logic of Verification - 51



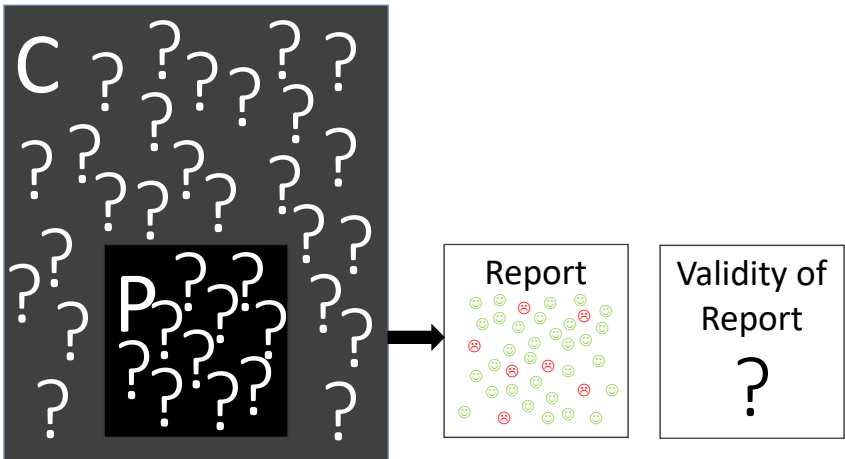
There are many quality criteria that cannot be  
checked easily: testability, maintainability...

The Logic of Verification - 52



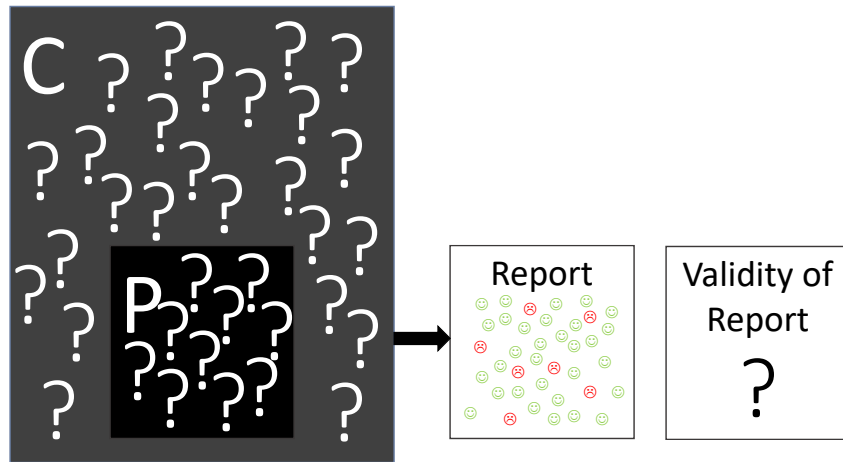
What are we doing to find problems that are not found by automated checking?

The Logic of Verification - 53

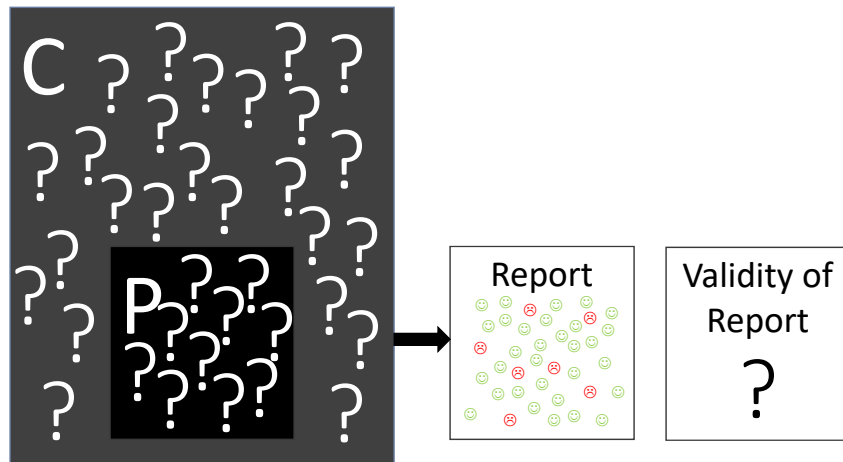


Many people say automated checking allows more time for “exploratory” testing. Is that true?

The Logic of Verification - 54

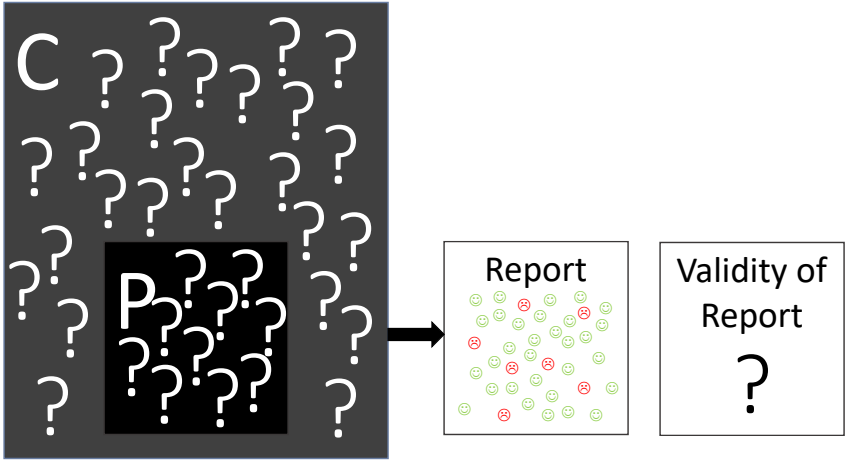


We MIGHT have more time for “exploratory” testing  
if checks are inexpensive, quick, and easy to prepare.



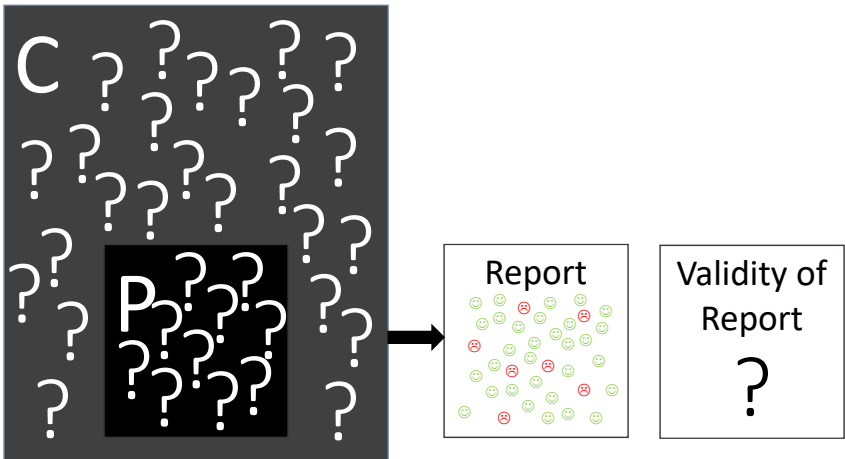
We MIGHT have more time for “exploratory” testing  
if we are not investigating too many “failing” checks.





But we might have fewer “failing” checks if we do more “exploratory” testing earlier!

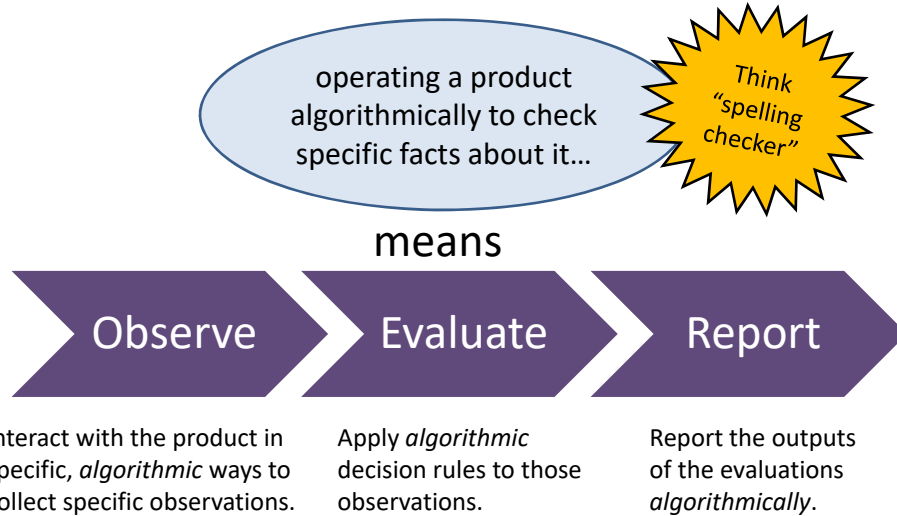
The Logic of Verification - 57



To understand how to do “exploratory” testing before checking, we must learn what testing really is.

The Logic of Verification - 58

## Call this “Checking” not Testing



The Logic of Verification - 59

## A check can be performed...



by a machine  
that *can't* think  
(but that is quick and precise)



by a human  
who has been told *not* to think  
(and who is slow and variable)

Notice that “quick” and “slow” refer only to the speed of observable behaviours and algorithmic evaluations.  
The machine is *infinitely* slow at recognizing unanticipated trouble.

The Logic of Verification - 60

## Testing Is *More Than* Checking

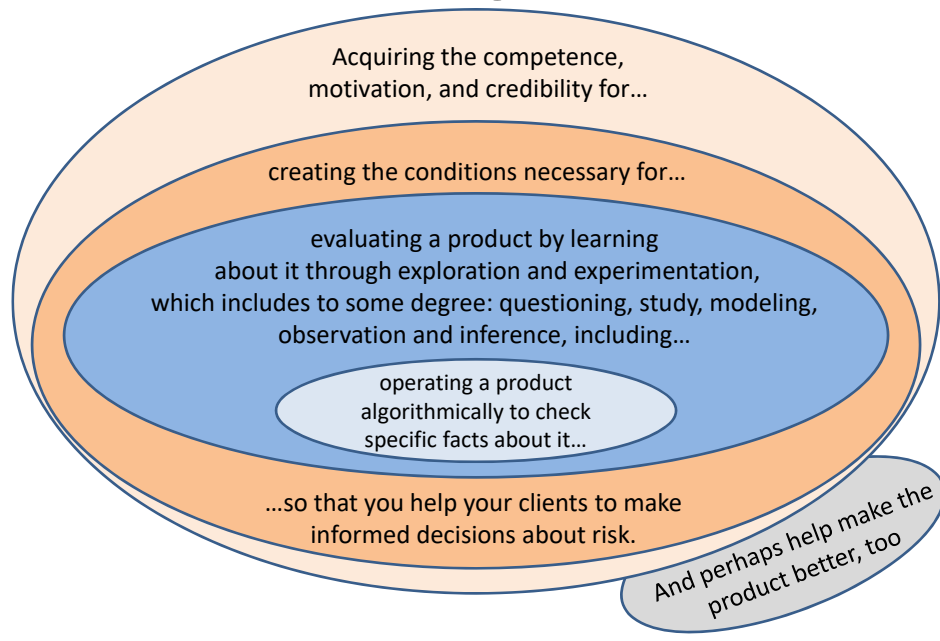
- *Checking* is okay, but it is mostly focused on confirming what we know or hope to be true.
- To escape problems with verification, we must do more than checking; we must *test*.



See <http://www.developsense.com/2009/08/testing-vs-checking.html>

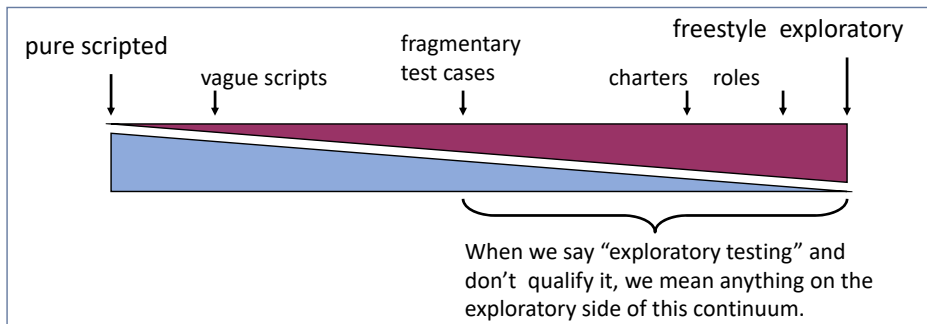
The Logic of Verification - 61

## Testing is...



## The Scripted/Exploratory Continuum, 2003

- When James Bach was describing testing in 2003, he put scripted testing on the left and exploratory testing on the right. Turns out there was a huge bug in this idea that we didn't notice *for years*.
- It looks like scripted testing comes first! But it doesn't!



James Bach: The Scripted/Exploratory Continuum from 2003

The Logic of Verification - 63

Where scripts  
(and checks)  
come from?

They come  
from  
exploratory  
work!

The Logic of Verification - 64

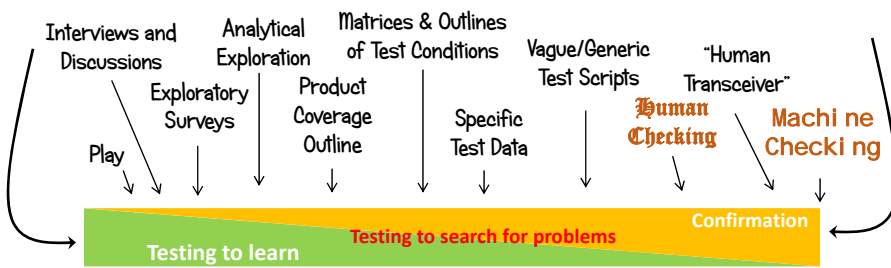
## The Formality Continuum, 2014

### INFORMAL

Not done in any specific way, nor to verify specific facts.

### FORMAL

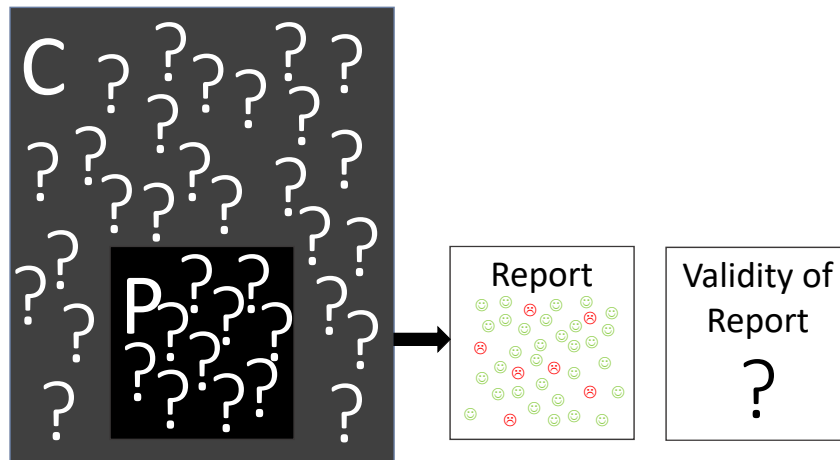
Done in a specific way, or to verify specific facts.



Loops of testing start with informal, exploratory work. If you want to do excellent formal testing (like automated checking), it must begin with excellent informal work.

Bug fix: formality tends to intensify over time, thus showing informality on the left and formality on the right makes more sense.

The Logic of Verification - 65



Now we can talk about what  
"doing exploratory testing earlier" means.

The Logic of Verification - 66

## Exploratory testing includes...

In other words...

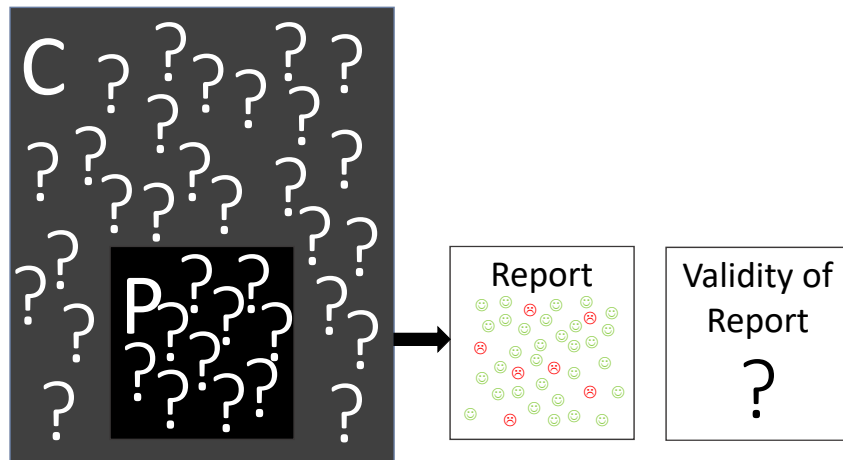
Exploratory testing is *testing*.

Verification is more like *demonstration*.

You need excellent exploratory work *before* you can do excellent verification.

Review  
and evaluation  
and learning  
and sensemaking  
and modeling  
and studying of the specs  
and risk analysis  
and recruiting of supporting testers  
and observation of the product  
and inference-drawing  
and questioning  
and task prioritization  
and coverage analysis  
and pattern recognition  
and pair development  
and decision making  
and testability advocacy  
and design of the test lab  
and preparation of the test lab  
and test code development  
and tool selection  
and making test notes  
and preparing simulations  
and experimentation  
and interacting with developers  
and triage  
and bug advocacy  
and relationship building  
and product configuration  
and application of oracles  
and designing visualizations  
and spontaneous playful interaction with the product  
and discovery of new information  
and preparation of reports for management  
and recording of problems  
and investigation of problems and working out puzzling situations  
and building the test team  
and analyzing competitors  
and resolving conflicting information  
and benchmarking and...

The Logic of Verification - 67



How do we come to a better understanding of the status of the product and the quality of our checks?

The Logic of Verification - 68

If automated checking is to be valid, reliable, and cost-effective, it **MUST** be embedded in TESTING.

The Logic of Verification - 69

We must question, study, investigate, observe, diversify and challenge our models, checks, and reports, *and* our ideas about them.

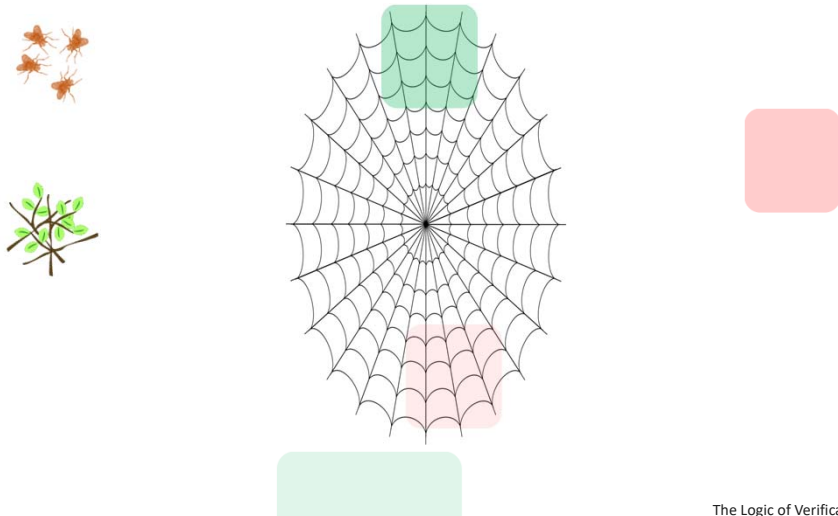
The Logic of Verification - 69

## Verifications Form a Web



The Logic of Verification - 71

## An *Imperfect* Web



The Logic of Verification - 72





“Researchers are increasingly coming to realise that social spiders also sort themselves according to their individual personalities...”

“By paying close attention to individual spiders, [researchers] have discovered that certain spiders are more likely to spend their days attacking predators, while others are more likely to repair the webs, help keep parasites away, clean the web, rear the young, and so on.”

<http://www.bbc.com/earth/story/20160122-meet-the-spiders-that-have-formed-armies-50000-strong>

The Logic of Verification - 73



## Way More Than Verification The Tester is the Spider in the Web



- Testers prepare, supervise, interpret, and maintain **checks and tests**.
- Testers **explore and play and learn** and build mental models of the product and its risks.
- Testers explain and justify their **strategy and status**.
- Testers seek and remove **blinds spots** in test strategy.
- Testers look for ways to refresh and improve the **value of the testing** over time.
- Testers adapt test strategy to the best current knowledge of **product risk**.
- Testers adapt test strategy to the **project context**.

The Logic of Verification - 74

## Critical Issues With Most Verification: Poor Sampling, Low Diversity and Weak Oracles.



## Workarounds to the Limits of Verification

- Instead of verification, consider *falsification*.
  - We CAN'T verify the idea that the product is okay, but we CAN falsify that idea.
- Instead of validation, consider *assessment*
  - To assess something is to develop opinions on it.
  - You can have opinions about all kinds of things that cannot be verified
  - Our goal is to develop an *informed* opinion of the product.
- Apply safety language
  - "We *have not seen* any bugs *so far*."
  - "We *are not aware* of any problems *yet*."

## Conclusions

- To test is not only to verify, but to *investigate* and to *challenge*.
- Excellent testing focuses on exploring and investigating many kinds of risk. Doing this requires many kinds of coverage—not only code coverage.
- Verification (in the form of automated checks or formal scripts performed by humans) may be useful, but it falls short of *testing*.
- Automating checks reduces execution time, but at some cost in development, maintenance, and interpretation.
- Automated checks can be used to test more broadly and more deeply—but there are many more ways to use tools.
- *Excellent* verification is *part* of a testing process that includes not only questioning of the product, but also questioning of the ways in which we check it and test it.

The Logic of Verification - 76

## A Word from Our Sponsor (Me)

- I teach Rapid Software Testing (RST).
- RST is a course, a mind-set, and a skill set about how to do **excellent software testing** in a way that is very **fast**, **inexpensive**, **credible**, and **accountable**.
- I teach RST in a class for testers.
- I also offer advice and consulting to managers and executives.

<http://www.developsense.com>