Spirent dX3 2-Port Seven-Speed 400/200/100/50/40/25/10GbE

Native QSFP-DD Test Module | Native CFP8 Test Module | Native OSFP Test Module

Network bandwidth needs continue to grow at a rapid pace. Network equipment manufacturers are developing highly flexible multi-rate products to support the latest generation of HSE devices. Service Providers and Hyperscale data centers are deploying multi-rate networking infrastructure solutions to meet this growing market.

With these multi-rate requirements, customers demand higher density test equipment. Flexibility is needed to validate the next generation of routers and data center fabrics.

Spirent dX3 quad speed module architecture was developed to meet these specific needs with its industry-leading 4 times density advantage for native CFP8 and 2 times density advantage for QSFP-DD and industry first OSFP test module.

Spirent's native QSFP-DD test modules can be configured to support seven speeds per port, 400/200/100/50/40/25/10GbE with both PAM4 and NRZ encoding. The native QSFP-DD test module also supports Auto Negotiation and Link Training for all speeds including 8x50G AN/LT. Trade-in programs are available for customers interested in upgrading existing test modules to support AN/LT and NRZ encoding.

For OSFP and native CFP8 versions, modules can be configured to support up to four speeds per port, 400/200/100/50GbE (PAM4).

As an additional benefit, DX3-QSFP-DD-2 test modules provide a convenient way to upgrade to new hardware speed options through the purchase of related software licenses. These next-generation modules do not need to be returned to the factory in order to upgrade support for new speed options. For more information, see Ordering Information section.

Applications

Cloud Computing/Streaming Services—Validate data plane QoS on thousands of flows at line rate and test complex routing, data center and access protocols on switches and routers. A single N12U can support 24-400G ports, or 4-ports from a single N4U chassis.

Data Center ToR and EoR Switches and Fabrics—Validate forwarding performance, latency, MAC capacity and functional capabilities of ultra-high-scale, next-generation enabled multi-terabit cloud data center fabrics.

Terabit Routers—Test 400 GbE core routers with high-scale, multiprotocol topologies.
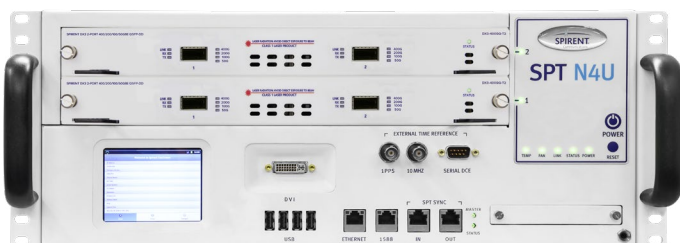
Features

- 2 400GbE ports per dX3 module, delivers the highest density high-speed Ethernet solution per module, chassis or rack unit

  - Each QSFP-DD port supports:
  - PAM4 - 1x400G, 2x200G, 4x100G, 8x50G
  - NRZ - 2x100G, 4x50G, 2x40G, 8x25G, 8x10G

- 4x100G (QSFP28) accessory cable ACC-1067A required

- Each port supports both PAM4 and NRZ encoding (Native QSFP-DD module ONLY, requires chassis license)

- Support for Ethernet (FEC), and Auto Negotiation and Link Training (AN/LT) on all speeds including 8x50G mode

- Protocol testing for L2/3 routing/switching and data center test cases

Benefits

- Industry's highest density single slot test modules: 4 times CFP8 advantage, 2 times QSFP-DD advantage

- Industry's first native OSFP test module

- PAM4 and NRZ (QSFP-DD) solution in one platform

- Provides large capacity testing for a variety of services

- Hardware speed option upgrades available via licensing

Productivity

- Intelligent Results™

- When creating test beds at the scale needed the amount of data that is produced is astronomical. An advanced, highly efficient distributed database processes billons of real-time results to validate tests and identify problems, giving engineers the immediate feedback they need to debug problems and accelerate development

- Delivers more results with tight correlation, and more information to find those obscure bugs. With more coverage and more information, Spirent answers questions faster, and in a single test run, where multiple runs are necessary with other test tools

- Interesting streams uses real-time results data mining to dynamically filter through mountains of data and display the results that matter

- Powerful automation with Command Sequencer (Visual Programming) and GUI to Script empowers the test operator to:
  - Construct sophisticated, stressful, automated test cases without programming experience
  - Combine numerous individual test cases into a single run to save regression test time
  - Develop a catalog of broad automated test cases in a fraction of the time
  - Export automated test cases to run from a command line for headless test execution that can be integrated with any automated regression system

Extensive, Flexible Reporting—Real-time statistics for critical variables across all protocols. Using Spirent's iTest platform, your device under test results can easily be correlated and compared with Spirent's results.

Technical Specifications				
Spirent dX3 Module				
Module Part Number	Speed	Maximum Ports per slot	Maximum Ports per SPT-N12U Chassis	Maximum Ports per SPT-N4U Chassis
DX3-400GS-T2	400/200/100/50/40/25/10GbE	PAM4 – 400/200/100/50GbE: 2/4/8/16 NRZ – 100/50/40/25/10GbE: 4/8/4/8/8	PAM4 – 400/200/100/50GbE: 24/48/96/192 NRZ – 100/50/40/25/10GbE: 48/96/48/96/96	PAM4 – 400/200/100/50GbE: 4/8/16/32 NRZ – 100/50/40/25/10GbE: 8/16/8/16/16
DX3-400GQ-T2	400/200/100/50GbE	2/4/8/16	24/48/96/192	4/8/16/32
DX3-400GO-T2	400GbE Only	2	24	4
DX3-200GO-T2	200GbE Only	4	48	8
DX3-400GD-T2	400/100GbE	2/8	24/96	4/16
DX3-400GDF-T2	400/50GbE	2/16	24/192	4/32
DX3-400GQ-P2	400/200/100/50GbE	2/4/8/16	24/48/96/192	4/8/16/32
DX3-400GO-P2	400GbE Only	2	24	4
DX3-200GO-P2	200GbE Only	4	48	8
DX3-400GD-P2	400/100GbE	2/8	24/96	4/16
DX3-400GDF-P2	400/50GbE	2/16	24/192	4/32
DX3-400GQ-O2	400/200/100/50GbE	2/2/4/8	24/24/48/96	4/4/8/16
DX3-400GO-O2	400GbE Only	2	24	4
DX3-QSFP-DD-2-825A	400/200/100/50/40/25/10GbE	2/4/8/16	24/48/96/192	4/8/16/32
DX3-QSFP-DD-2-750A	400/200/100/50GbE	2/4/8/16	24/48/96/192	4/8/16/32
DX3-QSFP-DD-2-400A	400GbE only	2	24	4
MSA Interface	QSFP-DD, OSFP or CFP8			
Operational modes	PAM4 – 400/200/100/50GbE; NRZ – 100/50/40/25/10GbE			
Port CPU	Stackable multi-core CPU			
User reservation	Per QSFP-DD port			
Test Port speed config	2 test port speed groups per blade			
Line clocking and packet time-stamping	Stratum-3 rated oscillator is the default time source. Transmit line clock is at the precise nominal Ethernet rate ± < 1 PPM on initial shipment. Accurate to ± 4.6 PPM 15 years of operation • Frame time-stamp resolution of 2.5ns • GPS and CDMA-based external time sources are supported • IEEE 1588v2 and NTP packet-based external time sources are supported • TIA/EIA-95B-based external time sources are supported			
Inter-module and inter-chassis time synchronization	Modules in the same chassis are phased-locked to the timing source of the control module. For more modules in separate chassis: • Spirent-patented self-calibrating inter-chassis timing chain using dedicated port on chassis control module delivers precise synchronization ± 20ns • Synchronization via external GPS or CDMA network • Using IEEE 1588 or NTP packet-based approaches • With TIS/EIA-95B timing inputs			

Spirent dX3 Module (cont'd)	
Module weight	3.219 kg, 5.45lbs.
Module predicted MTBF	56,330 hours. Hours of continuous operation
Operating temperature range	Supported for 41° to 95° F (5° to 35° C) ambient temperature. 20% to 80% relative humidity
Max power draw per module	Maximum of 450W per slot

Spirent TestCenter Layer 2-3 Generator and Analyzer	
Number of streams	• Stats/Streams @400/200/100/50/40/25/10GbE: Tx=8k, Rx=16k • Stream fields can be varied to create billions of flows
Frame transmit modes	Port based (rate per port), stream based (rate per stream), burst, timed
Min/max frame size (w/CRC)	60 to 16,004
Min/max Tx rates	1 packet per 3.43 seconds to 101% of line rate
Real-time Tx stream adjustments	Change rate and frame length settings without stopping the generator or analyzer for truly interactive, cause and effect analysis
Per-stream statistics analyzed in real time	Tx and Rx frame counts and rates • Tx and Rx Layer 1 byte counts and rates • Out of sequence errors • FCS errors and rate • Min, Max and Average Latency (4K streams) • Real Time Dropped Frame count
Per-port statistics analyzed in real time	Tx and Rx frame counts and rates • Tx and Rx Layer 1 byte counts and rates • Out of sequence errors • PRBS errors • FCS errors and rate
Transmit timestamp resolution	2.5 ns Tx timestamp resolution with intra-chassis and inter-chassis synchronization
Supported encapsulations	• Layer 2: Ethernet II, 802.1Q, 802.1ad, FCoE • Layer 3/4: IPv4, IPv6, TDP, UDP
Supported Tx signature capability	Fully compatible with Spirent hardware; contains sequence number and highly accurate timestamp
Capture buffer size	8 MB per port
Capture buffer controls— Spirent TestCenter's unique capture capability allows maximum effectiveness when debugging hard to find hardware or protocol problems	Several modes of operation that include: Filter by protocol fields, filter by byte offset and range; store slices or full-frames; store signature or all frames; store tx/rx control plane with data plane; real-time mode for control plane traffic; wrap or stop buffer at end. User defined pattern definitions can logically combine 8 filters of up to 32 total bytes. Patterns can be applied to start, filter (quality) or stop capture. In addition to user-patterns, filtering, starting and stopping capture contains the following pre-defined events: FCS, PRBS, IPv4 checksum, TCP/UDP/IGMP checksum, and sequence errors; undersize, oversize, jumbo, and user-defined frame length; IPv4, IPv6, TCP, UDP and IGMP packets; test signature present and test stream ID match. Each event can be independently set to ignore, include or exclude.
Latency modes	Benchmark tests support LIFO, LILO, FIFO or FILO latency calculation methods
Route Insertion Table (RIT) Entries per port	8K 4-byte entries for dynamic label or random IP/MAC address assignments
RIT or List VFD Entries per stream	8 RIT insertions per stream and 4 VFD insertions per stream

Layer 1 Functionality	
QSFP Interconnects	SR, LR, FR, DR, PSM4 at multi-rate (400/200/100/50/40/25/10GBE)
OSFP or CFP8 Interconnects	SR, LR, FR, DR, PSM4 at multi-rate (400/200/100/50GBE)
Media support and FEC options	Support varies by module speed mode • 400G: 400GBASE-SR16, 400GBASE-DR4, 400GBASE-LR8, 400GBASE-FR8, 400GBASE-LR4, 4x100G QSFP-DD LR • 200G: 200GBASE-SR4, 200GBASE-PSM4,200GBASE-LR/FR4, plus additional MSA PMDs • 100G: 100GBASE-SR2, 100GBASE-LR2 plus additional MSA PMDs • RS-FEC (544) KP all speeds • Direct Attach Cable breakouts **NRZ** support varies by module speed mode and license • 100G: 100GBASE-SR4, 100GBASE-CR4, 100GBASE-LR4, plus additional MSA PMDs • 50G: 25/50G Consortium 50GBASE-CR2, • 40G: 40GBASE-SR4, 40GBASE-CR4, 40GBASE-LR4 • 25G: 802.3by 25GBASE-CR, 25GBASE-CRS, 25GBASE-SR • 10G: 10GBASE-SR, 10G Copper DAC • QSFP28 to SFP28 breakout cable options • Auto-Negotiation and Link Training for 100G, 50G, 40G and 25G • Clause 74 BASE-R FEC, Clause 91 RS-FEC, and Clause 108 RS-FEC • 25/50G Consortium 50GBase-R FEC CL74, 25/50G Consortium 50GBase RS-FEC CL91 • IEEE 25GBASE CR CL74, CL108, CR-S CL74, SR FEC CL108 • 25/50G Consortium 25GBase-R FEC CL74, 25/50G Consortium 25GBase RS-FEC CL91
AN/LT (Enable/Disable)	Direct Attach Copper (DAC), AN/LT supported for all speeds including 8x50G mode
Layer-1 Debug Tools & Features	CR Tx Emphasis settings, Rx Eye view, FEC Counters, PRBS Gen/Check, Front-end L1 Summary Status, Xcvr MDIO access

Technical Specifications (cont'd)	
Layer 4-7 Application and Security	
IP Version Supported	IPv4 and IPv6
Encapsulation Protocols	802.1Q and 802.1 Q-in-Q
Transport Protocols	TCP, UDP
Data Protocols	HTTP, SIP and FTP, Unicast/Multicast RTSP and RAW TCP
Authentication Protocols	802.1x
Network Access Protocol	DHCP and PPPoE
Network Realism Fragmentation	Line speed limitation, network latency, packet loss and fragmentation
Video Protocols	RTSP/RTP, Multicast streaming, IGMPv2, IGMPv3 and MLDv2
Video Codecs	H.263 and H.264
Video Quality Measurement	MDI measurements along with additional statistics to detect picture quality
Voice Codecs	G711A, G711U, G.723.1, G726-32, G.728 and G729AB
Voice Protocols	SIP over UDP

Ordering Information		
Part Number	**Description**	**Spirent TestCenter**
Test Modules		
DX3-400GS-T2	Spirent dX3 400/200/100/50/40/25/10GBE QSFP-DD 2-Port	X
DX3-400GO-T2	Spirent dX3 400GBE ONLY QSFP-DD 2-Port	X
DX3-200GO-T2	Spirent dX3 200GBE ONLY QSFP-DD 2-Port	X
DX3-400GQ-T2	Spirent dX3 400G/200/100/50GBE QSFP-DD 2-Port	X
DX3-400GD-T2	Spirent dX3 400/100GBE QSFP-DD 2-Port	X
DX3-400GDF-T2	Spirent dX3 400/50GBE QSFP-DD 2-Port	X
DX3-400GO-P2	Spirent dX3 400GBE ONLY CFP8 2-Port	X
DX3-200GO-P2	Spirent dX3 200GBE ONLY CFP8 2-Port	X
DX3-400GQ-P2	Spirent dX3 400G/200/100/50GBE CFP8 2-Port	X
DX3-400GD-P2	Spirent dX3 400/100GBE CFP8 2-Port	X
DX3-400GDF-P2	Spirent dX3 400/50GBE CFP8 2-Port	X
DX3-400GO-O2	Spirent dX3 400GBE ONLY OSFP 2-Port	X
DX3-400GQ-O2	Spirent dX3 400G/200/100/50BE OSFP 2-Port	X
DX3-QSFP-DD-2-825A	Spirent dX3 400/200/100/50/40/25/10GBE QSFP-DD 2-Port	X
DX3-QSFP-DD-2-750A	Spirent dX3 400/200/100/50GBE QSFP-DD 2-Port	X
DX3-QSFP-DD-2-400A	Spirent dX3 400GBE QSFP-DD 2-Port	X
Additional Features		
UPG-NRZ-DX3-400G-T2**	Factory Upgrade NRZ Spirent 400G QSFP-DD 2-Port	
ACC-1067*	Active Copper Breakout QSFP-DD to 4XQSFP28 3M	
Spirent Chassis		
SPT-N12U-110	Spirent N12U chassis and controller with 110VAC power supplies	
SPT-N12U-220	Spirent N12U chassis and controller with 220VAC power supplies	
SPT-N4U-110	Spirent N4U chassis and controller with 110VAC power supplies	
SPT-N4U-220	Spirent N4U chassis and controller with 220VAC power supplies	

Requirements

- Spirent chassis and controller (see table)
- Windows-based workstation with 10/100/1000 Mbps Ethernet NIC; mouse and color monitor required for GUI operation
- Linux- or Windows-based workstation for scripting
- Mac-, Linux- or Windows- based workstation for Rest API support

* High density 100G QSFP28, also requires BPK-1378 QSFP-DD to 4xQSFP28 chassis license

** This feature requires 8x50G-AN/LT-compatible hardware. If hardware already supports 8x50G AN/LT, quote only UPG-NRZ-DX3-400G-T2.
If hardware does not support 8x50G AN/LT, then UPG-8x50G-ANLT-T2 needs to be added to quote (return to factory upgrade).